

PXE21100 User Manual

Rev. 1.2

Warranty Statement

Products sold by Tabor Electronics Ltd. are warranted to be free from defects in workmanship or materials. Tabor Electronics Ltd. will, at its option, either repair or replace any hardware products which prove to be defective during the warranty period. You are a valued customer. Our mission is to make any necessary repairs in a reliable and timely manner.

Duration of Warranty

The warranty period for this Tabor Electronics Ltd. hardware is one year, except software and firmware products designed for use with Tabor Electronics Ltd. Hardware is warranted not to fail to execute its programming instructions due to defects in materials or workmanship for a period of ninety (90) days from the date of delivery to the initial end user.

Return of Product

Authorization is required from Tabor Electronics before you send us your product for service or calibration. Call your nearest Tabor Electronics support facility. A list is located on the last page of this manual. If you are unsure where to call, contact Tabor Electronics Ltd. Tel Hanan, Israel at 972-4-821-3393 or via fax at 972-4-821-3388. We can be reached at: support@tabor.co.il

Limitation of Warranty

Tabor Electronics Ltd. shall be released from all obligations under this warranty in the event repairs or modifications are made by persons other than authorized Tabor Electronics service personnel or without the written consent of Tabor Electronics.

Tabor Electronics Ltd. expressly disclaims any liability to its customers, dealers and representatives and to users of its product, and to any other person or persons, for special or consequential damages of any kind and from any cause whatsoever arising out of or in any way connected with the manufacture, sale, handling, repair, maintenance, replacement or use of said products. Representations and warranties made by any person including dealers and representatives of Tabor Electronics Ltd., which are inconsistent or in conflict with the terms of this warranty (including but not limited to the limitations of the liability of Tabor Electronics Ltd. as set forth above), shall not be binding upon Tabor Electronics Ltd. unless reduced to writing and approved by an officer of Tabor Electronics Ltd. This document may contain flaws, omissions, or typesetting errors. No warranty is granted nor liability assumed in relation thereto. The information contained herein is periodically updated and changes will be incorporated into subsequent editions. If you have encountered an error, please notify us at support@taborelec.com. All specifications are subject to change without prior notice. Except as stated above, Tabor Electronics Ltd. makes no warranty, express or implied (either in fact or by operation of law), statutory or otherwise; and except to the extent stated above, Tabor Electronics Ltd. shall have no liability under any warranty, express or implied (either in fact or by operation of law), statutory or otherwise.

Proprietary Notice

This document and the technical data herein disclosed are proprietary to Tabor Electronics, and shall not, without express written permission of Tabor Electronics, be used, in whole or in part to solicit quotations from a competitive source or used for manufacture by anyone other than Tabor Electronics. The information herein has been developed at private expense and may only be used for operation and maintenance reference purposes or for purposes of engineering evaluation and incorporation into technical specifications and other documents, which specify procurement of products from Tabor Electronics.

Document Revision History

Table 1.1 Document Revision History

Revision	Date	Description	Author
1.2	16-Aug-2026	- Final version. - Release supporting PXE21100 Chassis Manager Setup Ver. 2.0.2, and Chassis Manager User Interface 3.0.1.0 or higher.	Netanel Krotovich Jakob Apelblat
1.1	01-Sep-2025	1.1 Scope: New note. 4 PXE2100 Chassis Manager: New section. Table 6.6 General: Added weight.	Netanel Krotovich Meitav Dahan Jakob Apelblat
1.0	09-May-2024	Original preliminary version.	Jakob Apelblat

Acronyms & Abbreviations

Table 1.2 Acronyms & Abbreviations

Acronym	Description
μs or us	Microseconds
ADC	Analog to Digital Converter
AM	Amplitude Modulation
ASIC	Application-Specific Integrated Circuit
ATE	Automatic Test Equipment
AWG	Arbitrary Waveform Generators
AWT	Arbitrary Waveform Transceiver
BNC	Bayonet Neill–Concelm (coax connector)
BW	Bandwidth
CW	Carrier Wave
DAC	Digital to Analog Converter
dBc	dB/carrier. The power ratio of a signal to a carrier signal, expressed in decibels
dBm	Decibel-Milliwatts. E.g., 0 dBm equals 1.0 mW.
DDC	Digital Down-Converter
DHCP	Dynamic Host Configuration Protocol
DSO	Digital Storage Oscilloscope
DUC	Digital Up-Converter
ENoB	Effective Number of Bits
ESD	Electrostatic Discharge
EVM	Error Vector Magnitude
FPGA	Field-Programmable Gate Arrays
GHz	Gigahertz
GPIB	General Purpose Interface Bus
GS/s	Giga Samples per Second
GUI	Graphical User Interface
HP	Horizontal Pitch (PXIe module horizontal width, 1 HP = 5.08mm)
Hz	Hertz

Acronym	Description
IF	Intermediate Frequency
I/O	Input / Output
IP	Internet Protocol
IQ	In-phase Quadrature
IVI	Interchangeable Virtual Instrument
JSON	JavaScript Object Notation
kHz	Kilohertz
LCD	Liquid Crystal Display
LO	Local Oscillator
MAC	Media Access Control (address)
MDR	Mini D Ribbon (connector)
MHz	Megahertz
MIMO	Multiple-Input Multiple-Output
ms	Milliseconds
NCO	Numerically Controlled Oscillator
ns	Nanoseconds
PC	Personal Computer
PCAP	Projected Capacitive Touch Panel
PCB	Printed Circuit Board
PCI	Peripheral Component Interconnect
PRBS	Pseudorandom Binary Sequence
PRI	Pulse Repetition Interval
PXI	PCI eXtension for Instrumentation
PXle	PCI Express eXtension for Instrumentation
QC	Quantum Computing
Qubits	Quantum bits
RADAR	Radio Detection And Ranging
R&D	Research & Development
RF	Radio Frequency
RT-DSO	Real-Time Digital Oscilloscope
s	Seconds
SA	Spectrum Analyzer
SCPI	Standard Commands for Programmable Instruments
SFDR	Spurious Free Dynamic Range
SFP	Software Front Panel
SMA	Subminiature version A connector
SMP	Subminiature Push-on connector
SPI	Serial Peripheral Interface
SRAM	Static Random-Access Memory
TFT	Thin Film Transistor
T&M	Test and Measurement
TPS	Test Program Sets
UART	Universal Asynchronous Receiver-Transmitter
USB	Universal Serial Bus

Acronym	Description
VCP	Virtual COM Port
Vdc	Volts, Direct Current
V p-p	Volts, Peak-to-Peak
VSA	Vector Signal Analyzer
VSG	Vector Signal Generator
WDS	Wave Design Studio

Contents

Document Revision History	3
Acronyms & Abbreviations.....	3
Contents.....	6
Figures.....	7
Tables.....	7
1 General	8
1.1 Scope	8
1.2 Related Documentation	8
1.3 Maintenance.....	8
1.3.1 Preventive Maintenance.....	8
1.3.2 Long Term Storage or Repackaging For Shipment.....	8
1.4 Safety.....	9
2 Introduction	10
2.1 Front Panel	11
2.2 Rear Panel.....	12
3 Installation.....	14
3.1 Installation Overview.....	14
3.2 Unpacking and Initial Inspection	14
3.3 Safety Precautions.....	14
3.4 Operating Environment.....	15
3.5 Power Requirements.....	15
3.6 Grounding Requirements	15
3.7 Performance Checks.....	15
3.8 Long Term Storage or Repackaging for Shipment.....	15
3.9 Installation	16
3.9.1 Rack Mounting	16
3.9.2 Installing Instrument Drivers	18
3.9.3 Installing Proteus/Lucid Modules	18
3.10 Chassis Cooling	18
4 PXE2100 Chassis Manager.....	20
4.1 Installation of the PXE21100 Chassis Manager	20
4.2 Operation	22
5 Wave Design Studio (WDS)	27
6 PXE21100 Specifications	28
6.1 Characteristics	28
6.2 Reference Input.....	29
6.3 Trigger Input.....	29
6.4 Clock Input.....	29
6.5 Clock Output.....	30
6.6 General	30
6.7 Ordering Information	31
6.8 Ordering Information Options.....	31

Figures

Figure 2.1 PXE21100 21 Slot PXIe Chassis.....	11
Figure 2.2 PXE21100 Front Panel.....	11
Figure 2.3 PXE21100 Rear Panel	12
Figure 3.1 PXE21100 Feet	16
Figure 3.2 PXE21100 with Full Rack Mounting Kit	17
Figure 3.3 PXE21100 with Rear Rack Mounting Extenders.....	17
Figure 3.4 PXE21100 Front and Side Air Intake	19
Figure 3.5 PXE21100 Rear Fan Exhaust.....	19
Figure 4.1 Chassis Overview	22
Figure 4.2 Current Warnings.....	23
Figure 4.3 Trigger Routing.....	23
Figure 4.4 About PXE21100 Chassis Manger User Interface	24
Figure 4.5 Chassis Overview with 8 Populated Slots	24
Figure 4.6 Main Screen with Module information.....	26

Tables

Table 1.1 Document Revision History.....	3
Table 1.2 Acronyms & Abbreviations.....	3
Table 6.1 Characteristics	28
Table 6.2 Reference Input Specifications.....	29
Table 6.3 Trigger Inputs Specifications	29
Table 6.4 Clock Input Specifications	29
Table 6.5 Clock Output Specifications	30
Table 6.6 General	30
Table 6.7 Ordering Information	31
Table 6.8 Ordering Information Options	31

1 General

1.1 Scope

The scope of this manual is to describe the setup, operating procedures, and specifications of the Tabor Electronics PXE21100 PXIe based (PCI Express eXtension for Instrumentation) 21 slot Gen 4 x 8 chassis.

Note

The chassis is not fully PXIe compliant.

1.2 Related Documentation

- Wave Design Studio User Manual
- Proteus Programming Manual
- Proteus Module User Manual
- Lucid-X PXIe User Manual
- PXI-5 PXI Express Hardware Specification

1.3 Maintenance

1.3.1 Preventive Maintenance

No periodic preventive maintenance is required.

1.3.2 Long Term Storage or Repackaging For Shipment

If the instrument is to be stored for a long period of time or shipped immediately, proceed as directed below. If you have any questions, contact your local Tabor Electronics representative or the Tabor Electronics Customer Service Department.

Repack the instrument using the wrappings, packing material and accessories originally shipped with the unit. If the original container is not available, purchase replacement materials. Be sure the carton is well sealed with strong tape or metal straps. Mark the carton with the model and serial number. If it is to be shipped, show sending and return address on two sides of the box. If the instrument is to be shipped for service or repair, the following information must be included with the shipment:

- Name and address of the owner.
- Record the model and serial number of the instrument, options, and firmware version.
- Note the problem and symptoms – detailed information will help in verifying the problem
 - What was the instrument setup, such as the run mode, arbitrary/task mode, task table etc.
 - Did the unit work; then fail or was it dead on arrival.
 - What other equipment was connected to the generator when the problem occurred, such as external trigger or clock.

- The name and telephone number of someone familiar with the problem can be contacted by Tabor Electronics if any further information is required.
- Show the returned authorization order number (RMA) as well as the date and method of shipment.

Note

Always obtain a return authorization number from the factory before shipping the instrument to Tabor Electronics.

1.4 Safety

To avoid electrical shock, fire or personal injury:

- Use only the proper power cord and certified for the country of use.
- This product is grounded through the grounding conductor of the power cord. To avoid electrical shock, the grounding conductor must be connected to the ground. Before connecting to the power input or output, ensure that the product is properly grounded.
- Do not operate this product with removed covers or panels.
- Observe all the ratings and markings on the product. Search this manual for further rating information, before connecting to it. Do not apply potential that is higher than the maximum rating.
- Do not operate in dark or wet conditions.
- Do not operate in an explosive environment. Keep product clean and dry.

2 Introduction

The Tabor Electronics' PXE21100, 21 slot PXIe based chassis is designed to support Tabor's current and future PXIe based products as well as 3rd party PXIe compliant modules. With a powerful and built-in controller inside the chassis it is the market's first chassis to offer 21 PXIe slots, as well as the first to support up to PCIe Gen 4 x 8 interfaces. With an advanced cooling design, the 21-slot, 4U chassis, utilizes every inch of a 19" rack space, enabling the highest PXIe slot count within a single chassis. The chassis delivers the performance, scalability and reliability for the most advanced and demanding applications.

Note

The chassis is not fully PXIe compliant.

Highlights

- Maximize rack space with the 21-slot PXIe chassis.
- Hosts the Tabor Lucid-X family of analog signal generators, the Proteus family of AWG/AWTs, and the TE320x family of PXIe RF amplifiers.
- Fastest available data transfer speeds with Gen 4 x 8 PCIe support.
- Advanced, powerful, and upgradeable built-in controller that eliminates the need for a high-performance external PC.
- Easy synchronization and scaling to hundreds of channels of Tabor Proteus series.
- Scale to large systems with multiple synchronized chassis.

Dedicated Tabor Product Support

The new chassis supports Tabor's Proteus AWG/AWT series and new Lucid-X RF signal generators series as well as the PXIe based RF amplifiers. Easily scale up to tens of synchronized channels in a single chassis without the need for external equipment or cabling. With its proprietary backplane design simply slide in multiple Proteus series models and all the synchronization is done using simple SCPI commands.

Scale to Multiple Chassis

For advanced applications such as quantum computing or phased array radar, where hundreds of channels are needed, it is possible to connect multiple chassis for a fully synchronized and phase coherent system. The dedicated PXE21106, synchronization unit, can synchronize up to 6 chassis and enables data transfer between modules in different chassis. For more than 6 chassis simply connect multiple PXE21106 units.



Figure 2.1 PXE21100 21 Slot PXIe Chassis

2.1 Front Panel

Below is the front panel of the PXE21100 chassis:

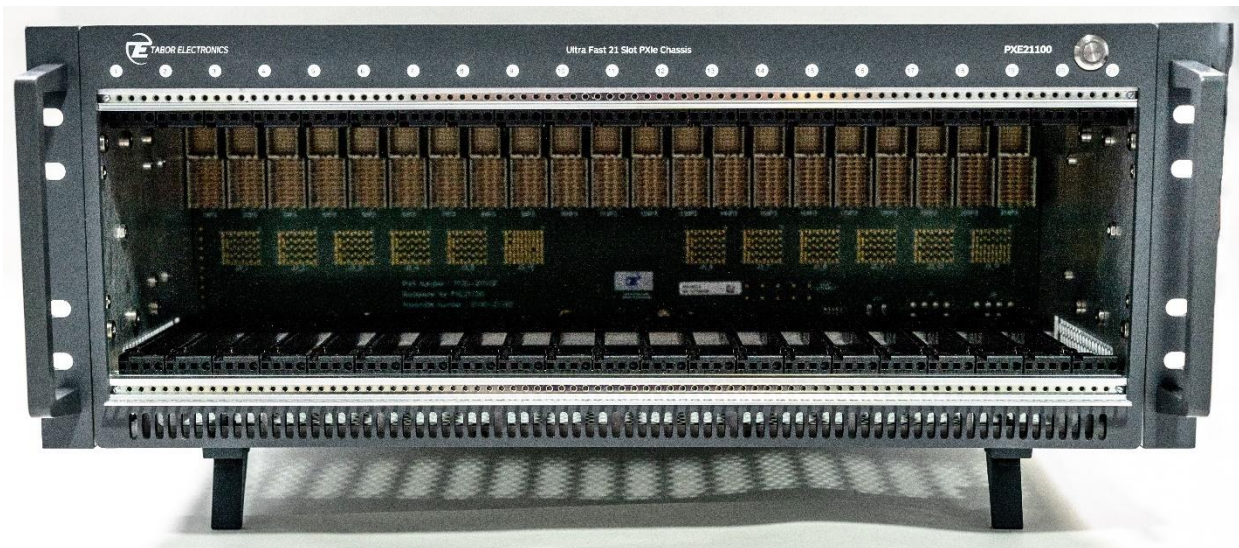


Figure 2.2 PXE21100 Front Panel

- **PXIe Gen4 8 Lanes Slot** – 21 slots for Proteus/Lucid-X modules.
- **1 – 21** – PXIe slot numbering.
- **LED/Power Button** — On/off power button. Green light indicates power on.

Note

Connector pin assignments of the PXI Express Peripheral Slots comply with the default pin assignments as defined in PXI-5 PXI Express hardware specification Rev.1.0.

2.2 Rear Panel

Below is the rear panel of the PXE21100 chassis:



Figure 2.3 PXE21100 Rear Panel

-  – Display Port for connecting an external display.
- **LAN** – 2.5GbE RJ45 connector for connecting a control PC via the LAN.
- **USB Host** – 2 x 2 x USB Type A (3 x USB 2.0, 1 x USB 3.2) interface for connecting a USB device such as a memory device (FAT32) for storing and recalling instrument setups, keyboard, or mouse.
- **SYNC IN** – Chassis sync in. D-Sub 9-Pin connector that is used to transfer the Tabor internal hardware and software signals between chassis.
- **SYNC OUT** – Chassis sync out. D-Sub 9-Pin connector that is used to transfer the Tabor internal hardware and software signals between chassis.
- **MESH CONTROL** – D-Sub 25-pin that connects the chassis to the external PXE21106 mesh device. This interface transfers the qubit decision bus to and from the mesh device.
- **FAN SPEED** –
 - **AUTO** – The fan speed is controlled by the chassis' hardware monitor IC.
 - **HIGH** – The fan speed is set to maximum regardless the temperature state.
- **REF IN** – 100 MHz, external reference in, SMA connector. This connector receives a 100MHz clock for synchronization purpose. You can select between the external reference clock and the internal 100 MHz oscillator.
- **TRIG IN** – Trigger input pulse from a 62.5MHz-150MHz clock, SMA connector. This signal is used as the synchronization trigger for multi chassis synchronization.
- **CLOCK IN** – 62.5 MHz – 150 MHz from the CLOCK OUT of another PXE21100, SMA connector. The signal is used as the synchronization clock for multi chassis synchronization.

- **CLOCK OUT** – Outputs clock of 62.5 MHz-150 MHz, SMA connector. The signal is used as the synchronization clock for daisy chain multi chassis synchronization.

Note

Only star connection with PXE21106 mesh device is supported.

- **MAINS INPUT** – 3 Pins IEC320 C14 inlet power plug socket, 100 - 240 VAC, 50/60 Hz. Internal fuse 16A.

3 Installation

3.1 Installation Overview

This chapter contains information and instructions necessary to prepare the PXE21100 chassis for operation. Details are provided for initial inspection, grounding safety requirements, repackaging instructions for storage or shipment, and installation information.

3.2 Unpacking and Initial Inspection

Unpacking and handling of the device requires normal precautions and procedures applicable to handling of sensitive electronic equipment. The contents of all shipping containers should be checked for included accessories and certified against the packing slip to determine that the shipment is complete.

The PXE21100 chassis is supplied with:

- Power cord with a plug according to customer country standard.
- USB Type A cable for connecting a control PC to the instrument.

Caution!

The PXE21100 chassis ships in an antistatic package to prevent damage from electrostatic discharge (ESD). When storing the unit, use the antistatic case.

3.3 Safety Precautions

This product is intended for use by qualified persons who recognize shock hazards and are familiar with the safety precautions required to avoid possible injury. The following sections contain information and cautions that must be observed to keep the device operating in a correct and safe condition.

**Caution**

For maximum safety, do not touch the product, test cables, or any other instrument parts while power is applied to the circuit under test. ALWAYS remove power from the entire test system before connecting cables or jumpers, installing, or removing cards from the chassis. Do not touch any object that could provide a current path to the common side of the circuit under test or power line (earth) ground. Always keep your hands dry while handling the instrument.

**Caution**

Carefully read the Safety Precautions instructions that are supplied with your test fixtures. Any adjustment, maintenance, and repair of an opened, powered-on instrument must be performed by authorized service personnel.

3.4 Operating Environment

The device is intended for indoor use and should be operated in a clean, dry environment with an ambient temperature within the range of 0°C to 55°C.

 Warning

The PXE21100 chassis must not be operated in explosive, dusty, or wet atmospheres. Avoid installation of the module close to strong magnetic fields.

The design of the device has been verified to conform to EN 61010-1 safety standard per the following limits: Installation (Overvoltage) Category I (Measuring terminals) Pollution Degree 2 Installation (Overvoltage) Category I refers to signal level, which is applicable for equipment measuring terminals that are connected to source circuits in which measures are taken to limit transient voltages to an appropriately low level. Pollution Degree 2 refers to an operating environment where normally only dry non-conductive pollution occurs. Occasionally a temporary conductivity caused by condensation must be expected.

3.5 Power Requirements

The chassis may be operated from a wide range of mains voltage from 100 to 240 VAC. Voltage selection is automatic and does not require switch setting. The chassis operates over the power mains frequency 50/60 Hz. Always verify that the operating power mains voltage is the same as that specified on the rear panel.

The device should be operated from a power source with neutral or near ground (earth potential). The instrument is not intended for operation from two phases of a multi-phase ac system or across the legs of a single-phase, three-wire AC power system. Crest factor (ratio of peak voltage to RMS) should be typically within the range of 1.3 to 1.6 at 10% of the nominal RMS mains voltage.

3.6 Grounding Requirements

To ensure the safety of operating personnel, the U.S. O.S.H.A. (Occupational Safety and Health) requirement and good engineering practice mandate that the instrument panel and enclosure be “earth” grounded.

3.7 Performance Checks

The chassis has been inspected for mechanical and electrical performance before shipment from the factory. It is free of physical defects and in perfect electrical order. Check the instrument for possible damage in transit and perform the electrical procedures outlined in the section entitled Unpacking and Initial Inspection.

3.8 Long Term Storage or Repackaging for Shipment

If the instrument is to be stored for a long period of time or shipped immediately, proceed as directed below. If you have any questions, contact your local Tabor Electronics representative or the Tabor

Electronics customer service department.

- Repack the instrument using the wrappings, packing material and accessories originally shipped with the unit. If the original container is not available, purchase replacement materials.
- Be sure the carton is well sealed with strong tape or metal straps.
- Mark the carton with the model and serial number. If it is to be shipped, show sending and return address on two sides of the box.

Note

If the instrument is to be shipped to Tabor Electronics for calibration or repair, attach a tag to the instrument identifying the owner. Note the problem, symptoms, and service or repair desired. Record the model and serial number of the instrument. Show the returned material authorization (RMA) order number as well as the date and method of shipment. Always obtain an RMA number from the factory before shipping the instrument to Tabor Electronics.

3.9 Installation

The chassis must be installed in a way that clears air passage to its cooling fans. For inspection and normal bench operation, place the instrument on the bench so it is clear of any obstructions to the rear fan to ensure proper airflow.

3.9.1 Rack Mounting

The chassis is supplied with front tilt stands and fixed feet. These are used for benchtop operation. If the chassis need to be stacked in a 19" cabinet, the feet must be removed before mounting in the rack. You can use a Phillips screwdriver to remove the feet.

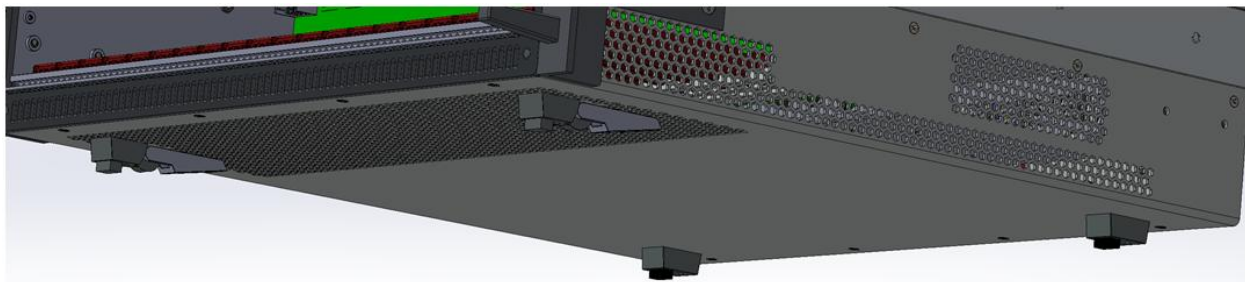


Figure 3.1 PXE21100 Feet

When the chassis is mounted in a rack, you must use the rear rack mounting wings and the rear mounting wings extender to support the weight of the box from the rear end. The full rack mounting kit is shown in the figure below (feet removed).

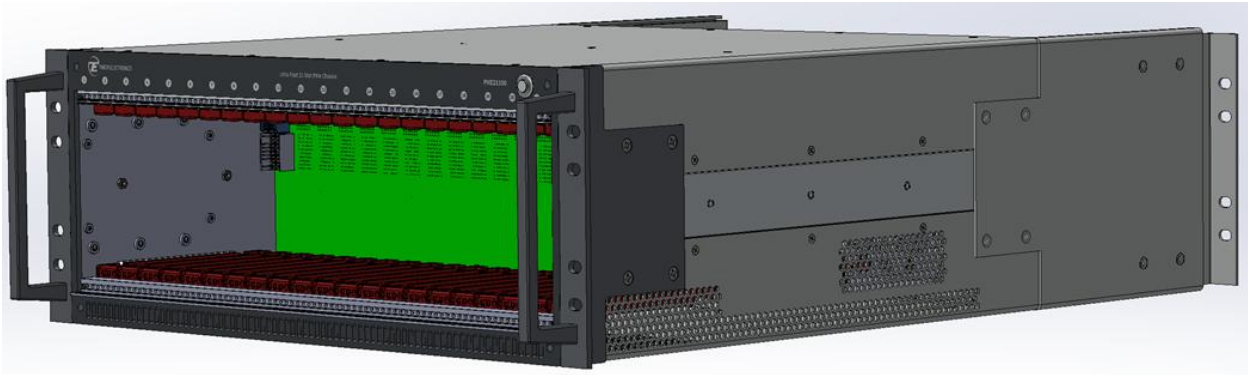


Figure 3.2 PXE21100 with Full Rack Mounting Kit

The optional rack mounting kit comes with a rear rack mounting extenders that fits a 630.0 mm (24.803") distance between the front and rear mounting rods. For deeper cabinets one must specify with the order the exact measure between the mounting rods. See figure below.

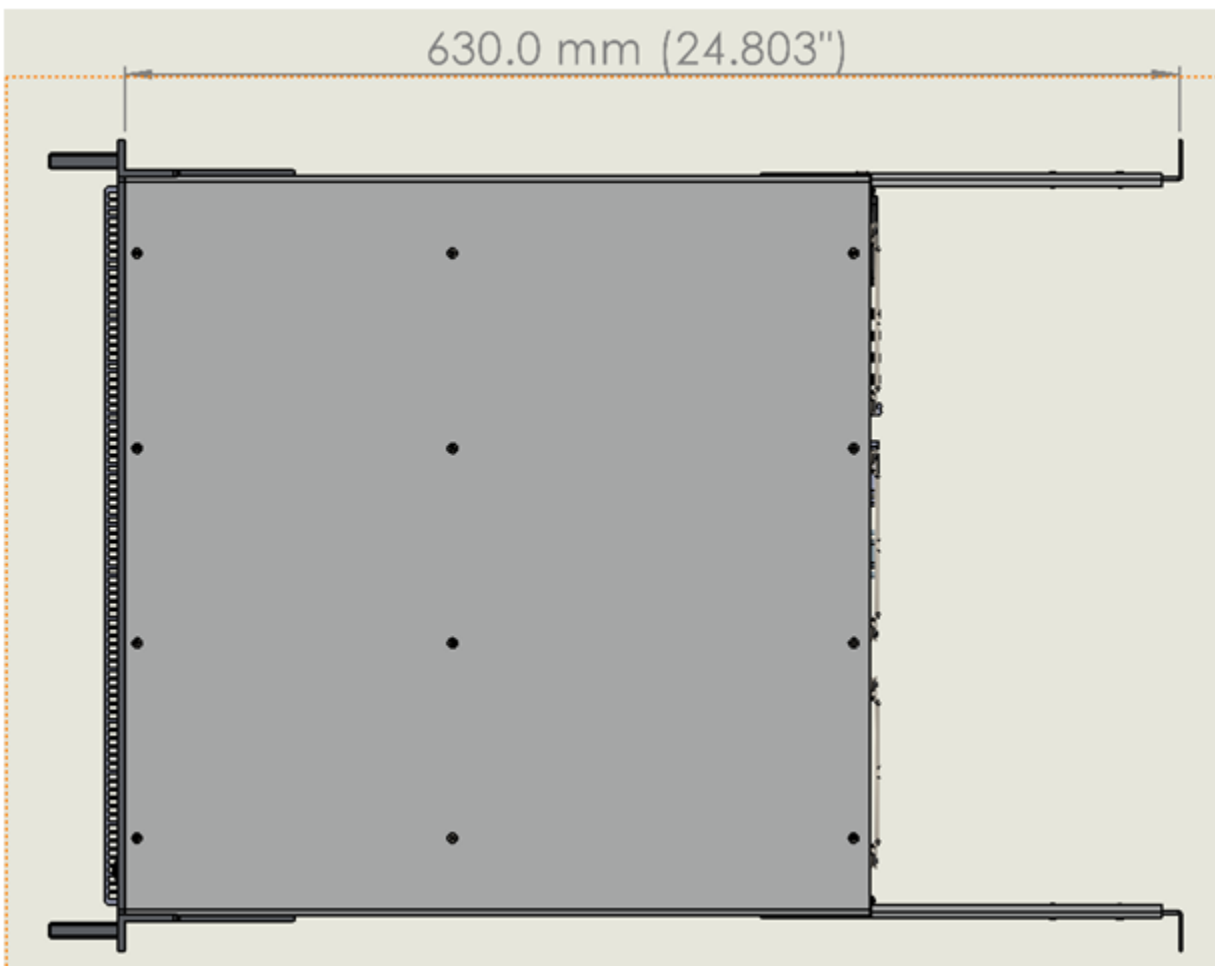


Figure 3.3 PXE21100 with Rear Rack Mounting Extenders

**Warning**

Once the chassis is installed in the chassis cover all remaining open slots to ensure proper airflow. Using the device without proper airflow will result in damage to the instrument. It is also recommended to use the highest fan setting available on the chassis to ensure proper cooling of the PXE21100 chassis.

3.9.2 Installing Instrument Drivers

The PXE21100 chassis necessary installation drivers, control software and relevant documentation can be downloaded from the Tabor Electronics website at <http://www.taborelec.com/downloads>. Follow the instructions below to install all the necessary drivers and DLLs on your PC to communicate and control your Proteus device.

Note

Check the Tabor Electronics website for the most recent software, driver, firmware, and documentation updates. www.taborelec.com/downloads.

3.9.3 Installing Proteus/Lucid Modules

The PXE21100 supports up to 21 modules.

1. Select an available slot (1 to 21)
2. Depress the module's latch and align the module's top and bottom edges with the card guides.
3. Carefully slide the module into the chassis.
4. Lift the latch until the module is securely seated in the chassis' backplane.
5. Tighten the screws on the module's front panel.

Note

To improve efficiency of heat dissipation, you should install filler plates for all unused slots.

3.10 Chassis Cooling

The PXE21100 has three 224 CFM (Cubic Feet per Minute) fans that provide a total airflow of up to 672 CFM. These fans are mounted on the chassis rear panel and exhaust air out the rear of the chassis. The air intakes are in the front, and sides of the chassis.



Figure 3.4 PXE21100 Front and Side Air Intake



Figure 3.5 PXE21100 Rear Fan Exhaust

4 PXE2100 Chassis Manager

4.1 Installation of the PXE21100 Chassis Manager

1. Connect a monitor to the DP (Display Port) connector, and a keyboard and mouse to the USB ports of the chassis.
2. For Proteus and Lucid-X instruments verify that the WDS (Wave Design Studio) application is installed in the PXE21100 embedded PC, refer to [5 Wave Design Studio \(WDS\)](#).
3. Copy the “PXE21100 Chassis Manager Setup Ver. x.y.z.msi” Windows Installer Package to the PXE21100 embedded PC.
4. Double-click the “PXE21100 Chassis Manager Setup Ver. x.y.z.msi” Windows Installer Package. The Welcome to the PXE21100 Chassis Manager screen is displayed.

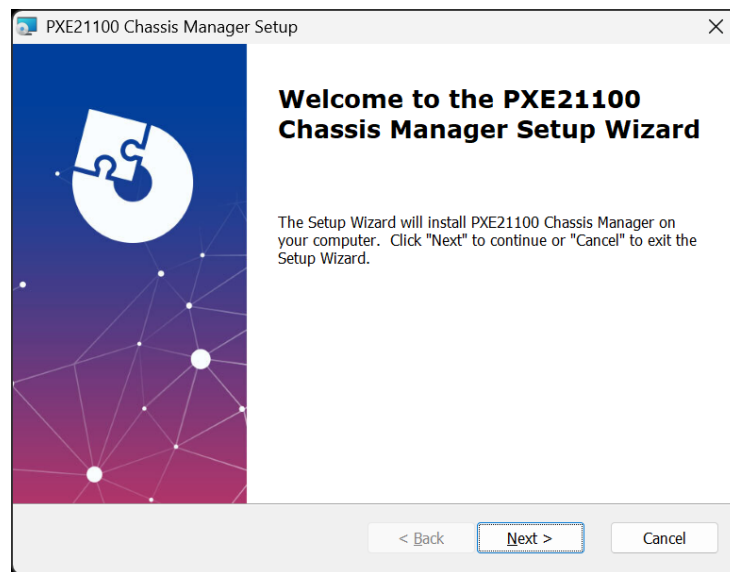
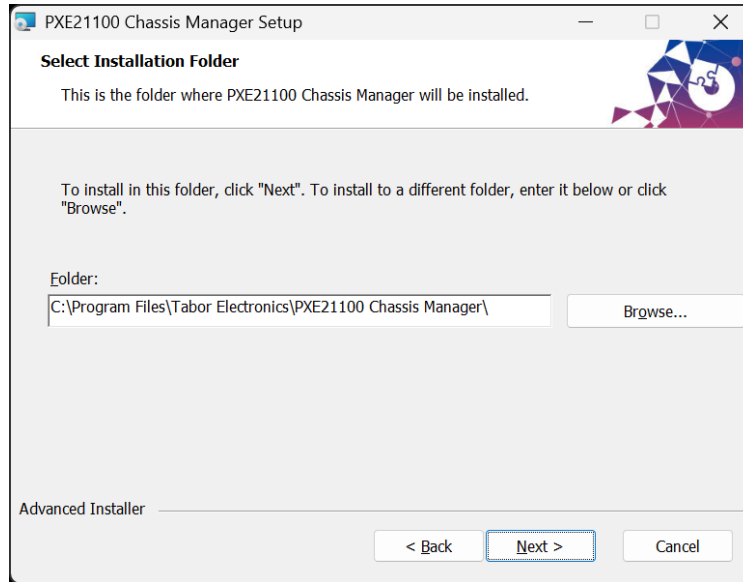
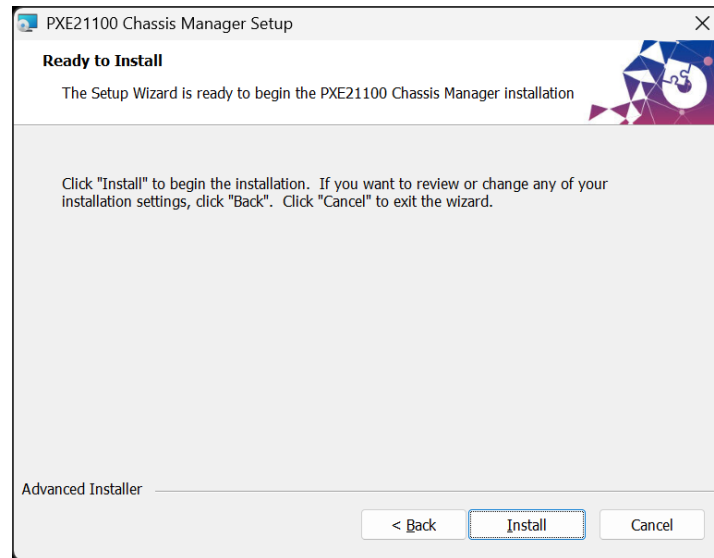


Figure 2.1 Welcome to the PXE21100 Chassis Manager

5. Click **Next**. The Select Installation Folder screen is displayed.

**Figure 2.1 Select Installation Folder**

6. Enter the destination directory for the PXE21100 Chassis Manager software or browse to a destination directory by clicking the **Browse** button.
7. Click **Next**. The Ready to Install screen is displayed.

**Figure 2.1 Ready to Install**

8. Click **Install**. After the installation the Completing the PXE21100 Chassis Manager Setup Wizard screen is displayed.

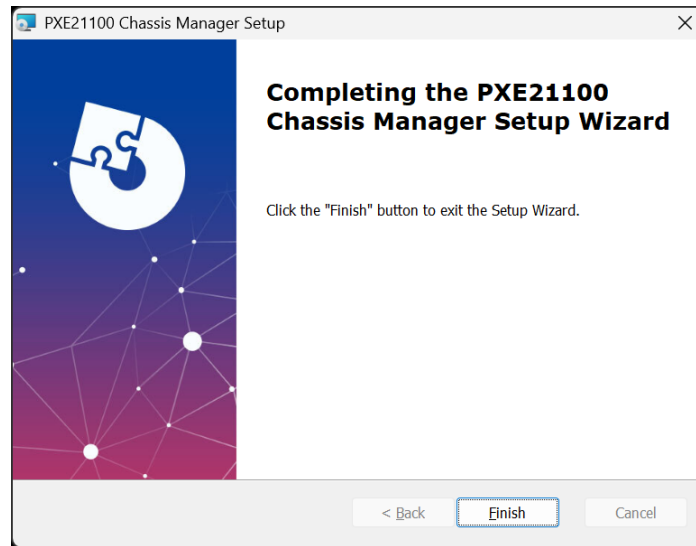


Figure 2.1 Completing the PXE21100 Chassis Manager Setup Wizard

4.2 Operation



Double-click on the Chassis Manager icon on your desktop to start the PXE21100 Chassis Manager. The Chassis Manager screen is displayed.

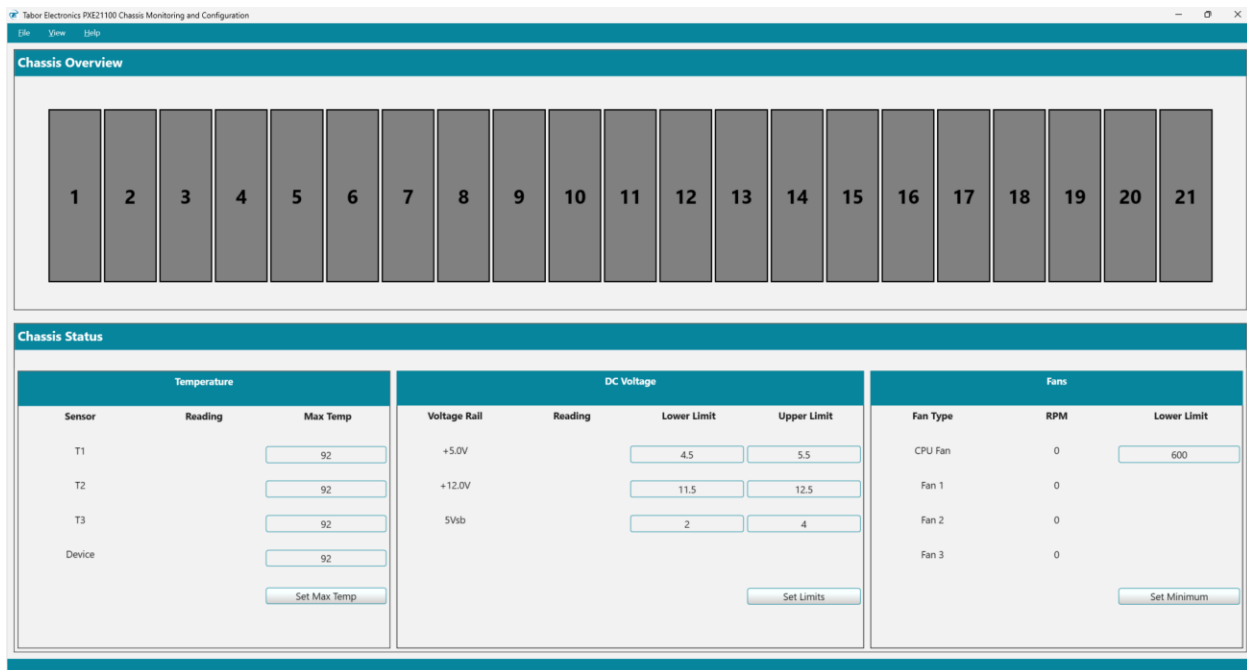


Figure 4.1 Chassis Overview

- **File** – Click the button to display a drop list.
 - **Open Log** – Shows a log of the chassis manager operational commands, warnings and errors.
 - **Exit** – Exit the PXE21100 Chassis Manager.

- **View** – Click the button to display a drop list.
- **Warnings** – Shows the current threshold warnings.

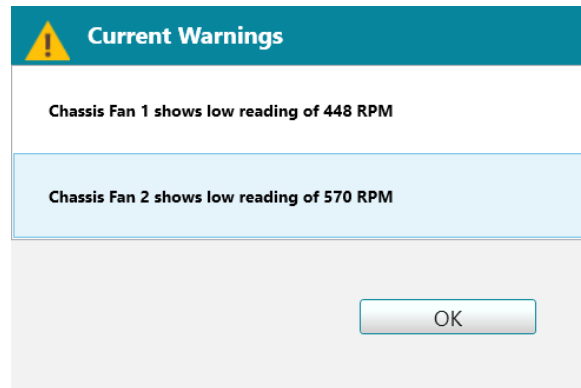


Figure 4.2 Current Warnings

- **Trigger Routing** – The PXE21100 offers eight PXIe backplane trigger lines as well as an external trigger input. The PXIe triggers are used for synchronizing operations between instruments. They can be used to initiate or stop instrument operation and sequence operations between multiple instruments.

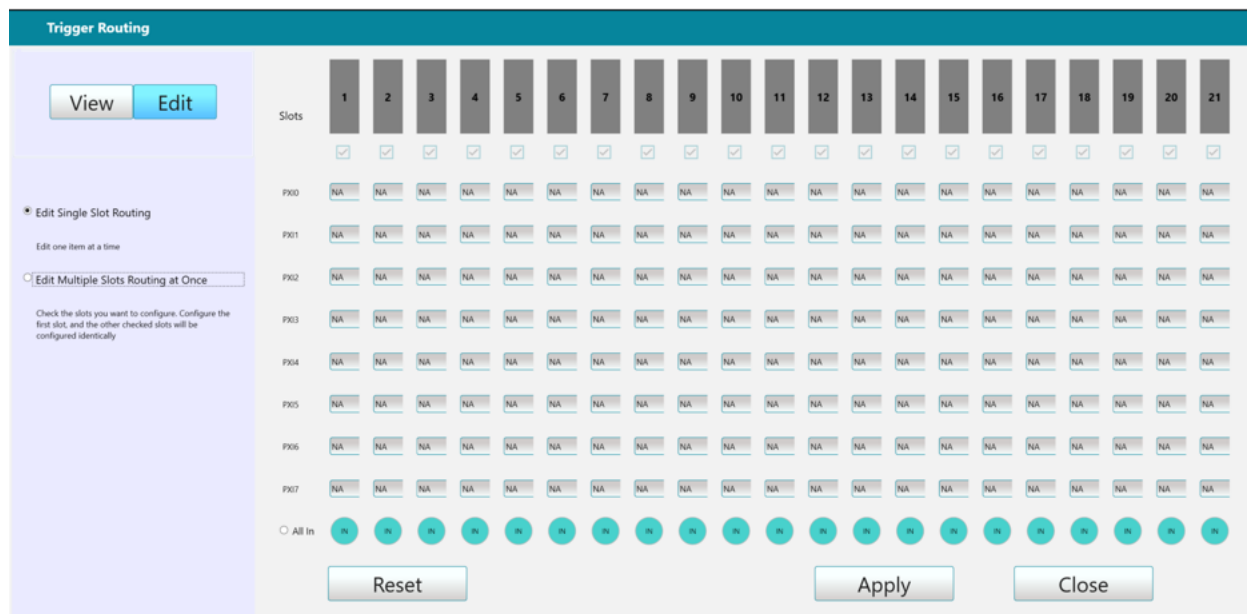


Figure 4.3 Trigger Routing

- **View** – Click the button to show the trigger routing.
- **Edit** – Click the button to configure the routing.
- **Edit Single Slot Routing** – Configure only the selected slot.

- **Edit Multiple Slot Routing at Once**– Check the slots you want to configure, configure the first slot and the other checked slots will be configured identically.
- **Slots 1 - 21** – Check the populated slot you want to trigger.
- **PX00 - PX07** – Select one of the eight trigger lines for the selected slot. Click the box and select from the drop list.
 - **NA** – Not applicable.
 - **CPU** – A trigger command from the CPU.
 - **EXT** – An external trigger from the chassis rear panel SMA connector.
- **Help** – Click the button to display a drop list.
- **About** – Show the application version.

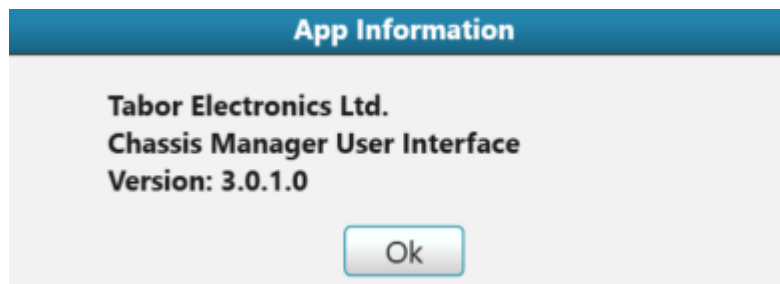


Figure 4.4 About PXE21100 Chassis Manger User Interface

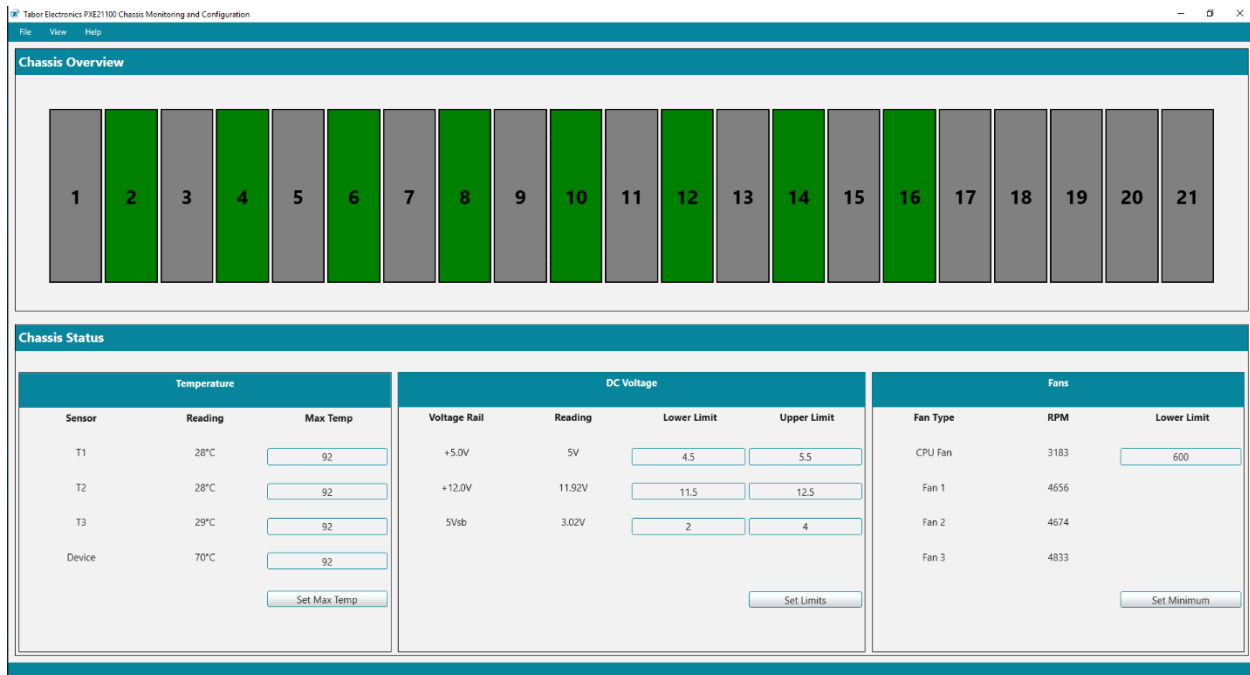


Figure 4.5 Chassis Overview with 8 Populated Slots

- **Chassis Overview**
 - **1 – 21** – Displays the 21 slots in the chassis. Populated slots are marked green.

- **Temperature** – The Temperature section monitors the temperature sensors in the chassis. If the temperature increases, the chassis manager will increase the fan's speed, and if the temperature reaches a critical limit, it will shut down the whole system to avoid fire and damage of the system.
 - **Sensor**
 - **T1, T2, T3** – Three temperature sensors located at different places in the chassis.
 - **Device** – The maximum temperature of the chassis PXIe devices.
 - **Reading**
 - The sensor temperature.
 - **Max Temp**
 - The maximum warning temperature threshold. A warning prompt will be issued if the threshold is reached. The system will shut down if the temperature has reached the system critical 96 °C.
 - **Set Max Temp** – Set the maximum temperature for the selected sensor.
- **DC Voltage**
- **Voltage Rail**
 - 5.0V, 12.0V, 5Vsb – The DC voltage rails in the chassis.
- **Reading**
 - The measured voltage of the voltage rail.
- **Lower Limit**
 - The minimum rail voltage required for the chassis operation. Violating the lower limit will cause a warning.
- **Upper Limit**
 - The maximum rail voltage required for the chassis operation. Violating the upper limit will cause a warning.
 - **Set Limits** – Set the thresholds for the selected voltage rails.
- **Fans**
 - **Fan Type**
 - **CPU Fan** – The embedded CPU fan.
 - **Fan 1 - Fan 3** – The chassis rear panel fan.
 - **RPM** – The fan speed as rotation per minute.
 - **Lower Limit** – The lower limit of the fan speed for a safe operation of the chassis. Violating the lower limit will cause a warning.
 - **Set Minimum** – Set the minimum threshold of the fan speed.

Click a populated slot to get information about the device in the slot, such as Slot Number, Serial Number, Model, FPGA version, and the Device Temperature.

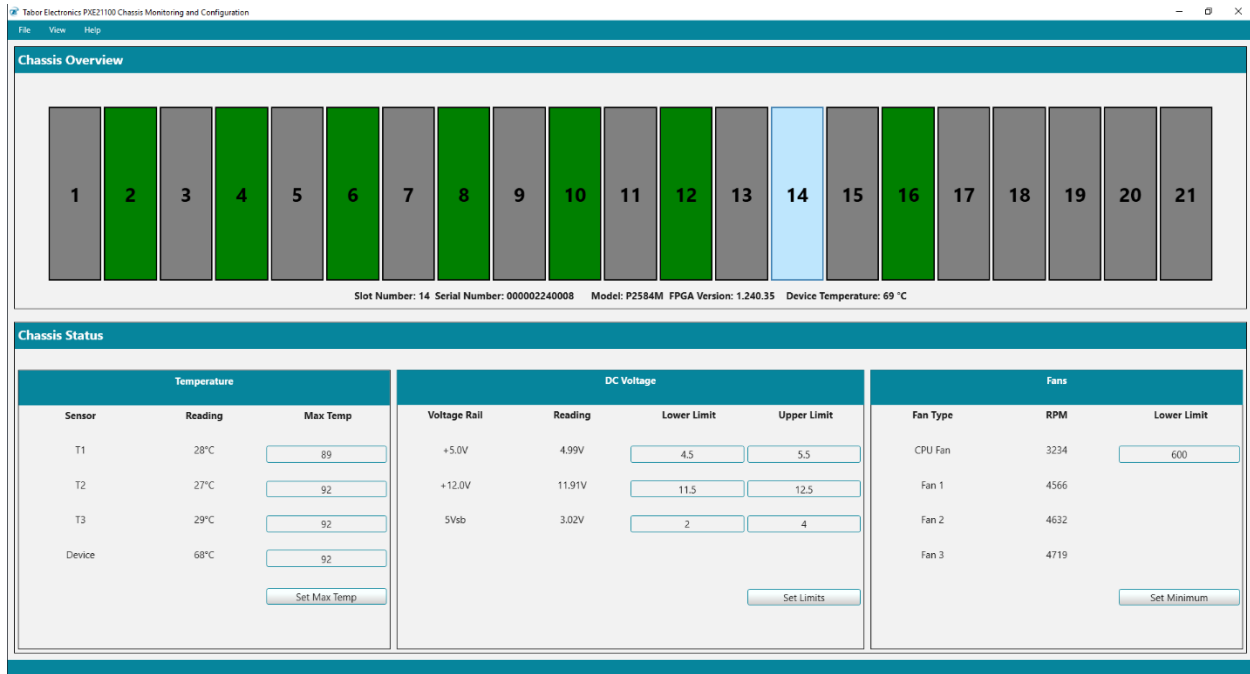


Figure 4.6 Main Screen with Module information

5 Wave Design Studio (WDS)

The Wave Design Studio (WDS) is a software package that enables full control and programming of your Tabor Electronics devices. It runs on a PC and provides a user-friendly graphical user interface. Use the TE Update Tool to update the Proteus device FPGA. The programs and the user manual can be downloaded from the Tabor Electronics website at <http://www.taborelec.com/downloads>.

6 PXE21100 Specifications

6.1 Characteristics

Table 6.1 Characteristics

Parameter	Description
Input Voltage Range	100 to 240 VAC
Operating Voltage Range	90 to 264 VAC
Input Frequency	50/60 Hz
Over Current Protection	Internal 16 A fuse in line
Power Consumption	
180 V-220 V Input Voltage	52 W max per slot
100 V-180 V Input Voltage	42 W max per slot
85 V-99 V Input Voltage	33 W max per slot
PXIe	21 slot PXIe Gen 4 x 8 lanes providing 128 Gb/s bandwidth per slot
PXIe Pin Assignment	PXI-5 PXI Express hardware specification Rev.1.0
Max. DC Current Consumption Per Slot	+3.3 V 6 A, +5 V 2 A,+12 V 4 A
CPU	Intel i5-13500E, 24 CPU threads, 24 MB Cache, 4.6 GHz (upgradeable)
Memory	8 GB (upgradeable)
Storage	128 GB SSD (upgradeable)
Operating System	Windows 10 IOT
USB	2 x 2 x USB Type A (3 x USB 2.0, 1 x USB 3.2)
Lan (Base-T)	1 x RJ-45 2.5GbE
Display	1 x Display Port

6.2 Reference Input

Table 6.2 Reference Input Specifications

Parameter	Description
Input Frequency	100 MHz external or 10 MHz internal
Lock Range	± 1 MHz
Input Level	+3 dBm to +14.5 dBm, 0.9-3.3 Vpp
Impedance	50 Ω , AC coupled (nom.)
Connector	SMA (female)

6.3 Trigger Input

Table 6.3 Trigger Inputs Specifications

Parameter	Description
Input level	LVCMOS 3.3 V
Input impedance	Hi-Z
Input trigger destination	PXI_Trig0 - PXI_Trig7
Input threshold	LVCMOS 3.3 V
Minimum swing	LVCMOS 3.3 V
Connector	SMA (female)

6.4 Clock Input

Table 6.4 Clock Input Specifications

Parameter	Description
Input Frequencies	62.5 MHz – 150 MHz configurable
Input Level Range	0.4 Vpp to 4 Vpp
Damage Level	4 Vpp
Input Impedance	50 Ω nom., AC coupled
Connector	SMA (female)

6.5 Clock Output

Table 6.5 Clock Output Specifications

Parameter	Description
Source	Selectable, internal DDS or external CLK_IN
Frequency Range	62.5 MHz – 150 MHz configurable
Output Amplitude	700 mVpp
Impedance	50 Ω (nom.), AC coupled
Connector	SMA (female)

6.6 General

Table 6.6 General

Parameter	Description
Weight	
Without Package	17.8 kg
Shipping Weight	20.8 kg
Dimensions	
With feet	438.8 x 191.7x 449.5 mm (W x H x D)
Without feet	438.8 x 176.0 x 449.5 mm (W x H x D)
Temperature	
Operating	0°C to +55°C
Storage	-40°C to +70°C
Altitude	
Operating	Up to 9,482 ft (2890 m)
Storage	Up to 15,000 ft (4572 m)
Relative Humidity Non-Condensing	10% to 90%
Safety:	CE Marked, EC61010-1:2010
EMC:	IEC 61326-1:2013

6.7 Ordering Information

Table 6.7 Ordering Information

Model	Description
PXE21100	21 slot PXle based chassis with embedded controller
PXE21106	1 U, 19" mesh controller for synchronizing up to 6 PXE21100 chassis

6.8 Ordering Information Options

Table 6.8 Ordering Information Options

Option	Description
COMP2	Upgrade for PXE21100 to i9-13900E Intel CPU, 32 CPU threads, 5.2 GHz, cache 36 MB, RAM 32 GB, 960 GB SSD
PXE21 RACKMOUNT KIT	Rear rack mounting wings and rear mounting wings extender for 630.0 mm (24.803") deep rack