WW5064/1074/2074 50MS/s, 100MS/s or 200MS/s Four Channel Arbitrary Waveform Generators

Specification

CONFIGURATION

Output Channels	4, semi-independent
STANDARD WAVE	FORMS
Waveforms:	Sine, Triangle, Square, Pulse, Ramp, Sine(x)/x, Gaussian, Exponential, Repetitive Noise and DC
Frequency Range:	
Sine	100µHz to 25MHz (WW5064)
	100µHz to 50MHz (WW1074)
	100µHz to 80MHz (WW2074)
Square, Pulse	100µHz to 12.5MHz (WW5064)
	100µHz to 25MHz (WW1074)
	100µHz to 50MHz (WW2074)
All others	100µHz to 6.25MHz (WW5064)
	100µHz to 12.5MHz (WW1074)
	100µHz to 25MHz (WW2074)
SINE	
	0.0409
Start Phase: Phase Resolution:	0-360° 0.01°
Harmonics Distortion	
DC to 2.5MHz	<-55dBc
2.5MHz to 25MHz	
25MHz to 40MHz	<-40dBc
40MHz to 80MHz	<-35dBc
Non-Harmonic Disto	rtion:
DC to 50MHz	<-70dBc
50MHz to 80MHz	<-65dBc
Total Harmonic Dist	ortion:
DC to 100kHz	0.1%
Flatness (1kHz)(typic	cal):
DC to 1MHz	1%
1MHz to 10MHz	3%
10MHz to 25MHz	5%
25MHz to 80MHz	10%
Phase Noise (8 point 100Hz Offset	
1kHz Offset	-80dBc/Hz -89dBc/Hz
10kHz Offset	-92dBc/Hz
100kHz Offset	-112dBc/Hz
1MHz Offset	-140dBc/Hz
TRIANGLE	
Start Phase Range:	0-360°
Phase Resolution:	0.01°
Timing Ranges:	0%-99.9% of period
SQUARE	•
Duty Cycle Range:	0% to 99.9%
Timing Ranges:	0%-99.9% of period
Rise/Fall Time:	<4ns (typ.)
Aberration:	<5%+10mV
SINC (Sine(x)/x)	

"0 Crossings":	4-100	
GAUSSIAN		
Time Constant:	10-200	
EXPONENTIAL PULSE		
Time Constant:	-100 to 100	
DC		
Range:	-5V to 5V	
PULSE		
Pulse Mode:	Single or double, programmable	
Polarity:	Normal, inverted or complement	
Period:		
WW5064	80ns to 1000s	
WW1074	40ns to 1000s	
WW2074	20ns to 1000s	
Resolution:		
WW5064	20ns	
WW1074	10ns	
WW2074	5ns	
Pulse Width:	100 to 1000-	
WW5064 WW1074	40ns to 1000s 20ns to 1000s	
WW2074	10ns to 1000s	
Rise/Fall Time:	10115 to 10005	
Fast	<4ns, typ. (WW5064)	
Tast	<6ns, typ. (WW1074)	
	<8ns, typ. (WW2074)	
Linear	20ns to 1000s (WW5064)	
Linoai	10ns to 1000s (WW1074)	
	5ns to 1000s (WW2074)	
High Time, Delay &		
Double Pulse Delay:	20ns to 1000s (WW5064)	
	10ns to 1000s (WW1074)	
	5ns to 1000s (WW2074)	
Impedance:	50 Ω	
Amplitude Window:	10mVp-p to 10Vp-p ⁽¹⁾	
Low Level	-5V to +4.995V ⁽¹⁾	
High Level	-4.995V to +5V ⁽¹⁾	
(1)	Double into high impedance	
NOTES:		
may be freely prog pulse period provic period and the sma not exceed the rati 2M/4M option, the (4,000,000) to 1, he do not show maxim computed from the 2.Rise and fall times,	rs, except rise and fall times, rammed within the selected led that the ratio between the illest incremental unit does o of 1,000,000 to 1. With the ratio is extended to 2,000,000 nce the specifications below hum limit as each must be e above relationship. may be freely programmed atio between the rise/fall	

provided that the ratio between the rise/fall time and the smallest incremental unit does not exceed the ratio of 100,000 to 1.

3.The sum of all pulse parameters must not exceed the pulse period setting

Function Character	Cine Trianala C
Function Shape:	Sine, Triangle, Square
Frequency Range: Phase (Sine/triangle):	0.01Hz to 1MHz 0 to 360°
Phase Resolution:	0.01°
Duty Cycle Range:	0% to 99.9%
Run Modes:	Continuous, Triggered
Delay Between Half	
(Continuous only):	200ns to 20s
Delay Resolution	20ns
ARBITRARY WAVE	FORMS
Sample Rate:	
WW5064	1.5S/s to 50MS/s
WW1074	1.5S/s to 100MS/s
WW2074	1.5S/s to 200MS/s
Vertical Resolution:	16 Bits
Waveform Memory: WW5064	F12k points (1M optional)
WW5064 WW1074/WW2074	512k points (1M optional) 1M points (2M/4M optional)
Min. Segment Size:	16 points
Resolution:	4 points
No. of Segments:	1 to 10k
SEQUENCED WAV	EFORMS
Operation:	Segments may be linked and
	repeated in a user-selectable
	order to generate extremely
	long waveforms. Segments
	are advanced using either a
	command or a trigger
Multi Sequence:	1 to 10, Selectable
Sequencer Steps:	1 to 4k 600ns min.
Segment Duration: Segment Loops:	1 to 1M
ADVANCE MODES	1 (0 110
Automatic:	No triggers required to step
	No triggers required to step from one segment to the
	No triggers required to step
	No triggers required to step from one segment to the next. Sequence is repeated
Automatic:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre-
Automatic:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external
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Automatic: Stepped:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external trigger advances to next programmed segment.
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Automatic: Stepped:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external trigger advances to next programmed segment. Current segment is sampled to the end of the segment including repeats and idles
Automatic: Stepped:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external trigger advances to next programmed segment. Current segment is sampled to the end of the segment including repeats and idles there. Next trigger advances
Automatic: Stepped: Single:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external trigger advances to next programmed segment. Current segment is sampled to the end of the segment including repeats and idles there. Next trigger advances to next segment
Automatic: Stepped:	No triggers required to step from one segment to the next. Sequence is repeated continuously through a pre- programmed sequence table Current segment is sampled continuously, external trigger advances to next programmed segment. Current segment is sampled to the end of the segment including repeats and idles there. Next trigger advances to next segment Each step of a sequence
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Specification

COMMON CHARACTERISTICS

FREQUENCY	
Resolution: Display Remote Accuracy/Stability:	11 digits (limited by 1μHz) 14 digits (limited by 1μHz) Same as reference
ACCURACY REFER	RENCE CLOCK
Internal External	0.0001% (1 ppm TCXO) initial tolerance over a 19°C to 29°C temperature range; 1ppm/°C below 19°C and above 29°C; 1ppm/year aging rate 10MHz TTL, 50% ± 2 %, or 50 $\Omega \pm 5$ % 0dBm (jumper)
AMPLITUDE	
Range: Resolution: Accuracy (1kHz): 16mV to 160mVp-p 160mV to 1.6Vp-p	10mV to 10Vp-p into 50Ω ; Double into open circuit 4 digits $\pm(1\% + 5mV)$ $\pm(1\% + 10mV)$
1.6V to 10Vp-p	±(1% + 70mV)
OFFSET	
Range: Resolution: Accuracy:	0 to ±4.995V, into 50Ω 1mV ±(1%+1% of Amplitude +5mV)
FILTERS	
Type: Bessel Elliptic	25MHz or 50MHz 60MHz or 120MHz
OUTPUTS	
MAIN OUTPUT	
Coupling: Connector: Impedance: Protection:	DC coupled Front panel BNC $50\Omega \pm 1\%$ Short Circuit to Case Ground, 10s max
SYNC OUTPUT	
Connector: Level: Sync Type:	Rear panel BNC TTL
Pulse LCOM Position:	Arbitrary and Standard waves Sequence and Burst modes
WW5064 WW1074/2074 Resolution:	0 to 512k (1M optional) 0 to 1M (2M or 4M optional) 4 points

SAMPLE CLOCK O	UTPUT
Connector: Level: Impedance:	Rear panel SMB 400mVp-p 50Ω
COUPLE OUTPUT	
Connector: Level: Impedance:	Rear panel SMB LVPECL 50Ω, terminated to +1.3V
INPUTS	
TRIGGER INPUT	
Connector: Input Impedance: Polarity: Level: Sensitivity: Damage Level: Min. Pulse Width:	Rear panel BNC 10kΩ Positive or negative, selectable ±5V 100mV ±12V 10ns
EXTERNAL REFER	ENCE INPUT
Connector: Frequency: Impedance & Level:	Rear panel SMB 10MHz
Default Option	10kΩ ±5%, TTL, 50% ±2% 50Ω ±5%, 0dBm Sinewave
SAMPLE CLOCK IN	IPUT
Connector: Range: WW5064 WW1074 WW2074 Input Level: Impedance: Min. Pulse Width:	Rear panel SMB 1.5Hz to 50MHz 1.5Hz to 100MHz 1.5Hz to 200MHz 300mVp-p to 1Vp-p 50kΩ 4 ns
COUPLE INPUT	
Connector: Input Level: Impedance: Min. Pulse Width:	Rear panel SMB LVPECL 50Ω, terminated to +1.3V 4 ns
RUN MODES	
Continuous: Triggered:	Free-run output of a waveform. Upon trigger, outputs one waveform cycle. Last cycle always completed.
Gated: Burst:	External signal transition enables or disables generator output. Last cycle always completed Upon trigger, outputs a Dual or multiple pre-programmed number of waveform cycles from 1 through 1M.

Mixed:	First output cycle is initiated by a software trigger. Consequent output requires external triggers through the rear panel TRIG IN
TRIGGER CHARAC	CTERISTICS
System Delay: Trigger Delay: Trigger Resolution: Trigger Delay Error:	6 SCLK + 150ns [(0; 200ns to 20s)+system delay] 20ns 6 SCLK + 150ns
EXTERNAL	
Source: Trigger Level: Resolution: Input Frequency: Min. Pulse Width: Slope: Trigger Jitter:	Rear panel BNC ±5V 1mV DC to 2.5MHz 10ns Positive/Negative, selectable ±1 sample clock period
INTERNAL / TIMEI	R
Range: Resolution: Error:	200ns to 20s 20ns 3 sample clock cycles+20ns
MANUAL	
Source:	Soft trigger command from the front panel or remote
FREQUENCY COU	INTER / TIMER
Measurements: Source: Range: Sensitivity: Accuracy: Slope: Gate Time: Input Range:	Frequency, Period, Averaged Period, Pulse Width & Totalize Trigger Input 10Hz to 100MHz (typ.120MHz) 500mVpp 1ppm Positive/Negative transitions 100µSec to 1 Sec ±5V



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INTER-CHANNEL DEPENDENCY

GENERAL

Separate controls: Common Controls:	Output on/off, amplitude, offset, standard waveforms, user waveforms, user waveform size, sequence table Sample clock (Arb), frequency (Std), period (Pulse) reference source, trigger modes, trigger advance source, SYNC OUT.
PHASE OFFSET (L	EADING EDGE)
DESCRIPTION:	Channel 1 used as start reference channel 2, 3 and 4 can be offset by a programmable number of points. Channels 3&4 must have the same duration in one of the following run modes: Triggered, Burst, or gated.
Jitter Between	
Channels:	0ps
Offset Range:	
WW5064	0 to ±512k points (1M opt.)
WW1074/WW2074	0 to ±1M points (2M/4M opt.)
Reference:	Each CH. in reference to CH 1
Resolution and Accu	-
Channels 1/2	1 point
Channels 3/4	4 points
Initial Skew:	<1ns
Error	1 SCLK

MULTI-INSTRUMENT SYNCHRONIZATION

<25 ns + 1 SCLK
Standard, Arbitrary and
Sequenced using the
automatic sequence advance
mode only
Continuous, Triggered, Gated
and Counted Burst

LEADING EDGE OFFSET

Run Mode:	Continuous run mode only
Offset Range:	200ns to 20s
Resolution:	20ns

Voltage Range:	85 to 265V
Frequency Range:	48 to 63Hz
Power Consumption:	40 to 03112
Display Type:	Color LCD, back-li
Size	3.8" reflective
Resolution	320 x 240 pixels,
Interfaces:	520 X 240 pixels,
USB Device	1 x rear, USB devi
LAN	100/10 BASE-T
GPIB	IEEE 488.2 standa
Dimensions:	IEEE 400.2 Stanua
With Feet	212 x 102 x 415m
Without Feet	212 x 88 x 415mm
Weight:	212 X 00 X 4151111
Without Package	3.5Kg
Shipping Weight	4Kg
Temperature:	4K <u>9</u>
	0°C - 50°C
Operating	-40°C to + 70°C.
Storage	-40 C l0 + 70 C.
Humidity:	050/
11°C - 30°C	85%
31°C - 40°C	75%
41°C - 50°C	45%
Safety:	EN61010-1, 2nd r
Calibration:	1 year
Warranty ⁽¹⁾ :	5 years standard

85 to 265V 48 to 63Hz 60W Color LCD, back-lit 3.8" reflective 320 x 240 pixels,
1 x rear, USB device, (A type) 100/10 BASE-T IEEE 488.2 standard interface
212 x 102 x 415mm (WxHxD) 212 x 88 x 415mm (WxHxD)
3.5Kg 4Kg
0°C - 50°C -40°C to + 70°C.
85% 75% 45% EN61010-1, 2nd revision 1 year

MODEL	DESCRIPTION
WW5064	50MS/s Four Channel Arbitrary Waveform Generator
WW1074	100MS/s Four Channel Arbitrary Waveform Generator
WW2074	200MS/s Four Channel Arbitrary Waveform Generator
OPTIONS	
WW5064:	
Option 1:	1M Memory (per channel
WW1074/WW2074:	
Option 1: Option 2:	2M Memory (per channel) 4M Memory (per channel)
ACCESSORIES	
Sync Cable: S-Rack Mount: D-Rack Mount: Case Kit:	Multi-instrument synchronization 19" Single Rack Mounting Kit 19" Dual Rack Mounting Kit Professional Carrying Bag
Note:	Options and Accessories must be specified at the time of your purchase.

⁽¹⁾ Standard warranty in India is 1 year.