

GENERAL CHARACTERISTICS		P9484/8/12B
NUMBER OF CHANNELS		
Standard		4/8/12
x1 Mode		2/4/6
MAX. SAMPLE RATE		9GS/s
MAX. INPUT DATA RATE		
Standard		2.5GS/s
x1 Mode		9GS/s
RESOLUTION		16-bit
ANALOG BANDWIDTH		8GHz
MAX. MEMORY SIZE		Up to 8GS

INTER-CHANNEL SKEW CONTROL	
INITIAL SKEW	<20ps
FINE DELAY	
RANGE	0 to 5 ns
RESOLUTION	5ps
ACCURACY	±5ps
COARSE DELAY	
RANGE	0 to wavelength
RESOLUTION	1 sample point

ARBITRARY MODE		
NUMBER OF SEGMENTS	64k	
MINIMUM SEGMENT LENGTH		
NORMAL	<u>x1 Mode</u> 2048 points	<u>Standard</u> 1024 points
FAST SEGMENT	224 points	64 points
WAVEFORM GRANULARITY		
STANDARD	<u>x1 Mode</u> 64 points	<u>Standard</u> 32 points
OPTIONAL	32 points	16 points

TASK MODE	
TASK TABLE LENGTH	64K tasks per channel
TASK LOOPS	1M
SEQUENCE ⁽¹⁾	
MAX. NUMBER OF SEQUENCES	32K sequences
SEQUENCE LOOPS	1M
SCENARIO ⁽²⁾	
MAX. NUMBER OF SCENARIOS	1K scenarios

STREAMING (STM OPTION)	
MAX. STREAM RATE	Up to 3GS/s
MINIMUM PC REQUIREMENTS	
CPU	i7
MEMORY	32GB
OPERATING SYSTEM	WINDOWS 10
SOURCE	Internal / Rear panel interfaces

SIGNAL PURITY	
HARMONIC DISTORTION ⁽³⁾	
f _{out} = 10 MHz - 200 MHz, Measured @ DC to 2 GHz	< -70 dBc (typ.)
f _{out} = 200 MHz ... 1.5 GHz, Measured @ DC to 4.5 GHz	< -60 dBc (typ.)
f _{out} = 1.5 GHz ... 4.5 GHz, Measured @ DC to 4.5 GHz	< -50 dBc (typ.)
SFDR ⁽⁴⁾	
f _{out} = 10 MHz...500 MHz, Measured @ DC to 1.5 GHz	< -85 dBc (typ)
f _{out} = 500 MHz...4.5 GHz , Measured @ DC to 4.5 GHz	< -75 dBc (typ)
PHASE NOISE (@10kHz offset)	
f _{out} = 140.625MHz	-134 dBc/Hz
f _{out} = 280.25MHz	-128 dBc/Hz
f _{out} = 562.5MHz	-122 dBc/Hz
f _{out} = 1.125GHz	-116 dBc/Hz
f _{out} = 2.25GHz	-110 dBc/Hz
f _{out} = 4.5GHz	-104 dBc/Hz

DIRECT OUTPUT	
OUTPUT TYPE	AC coupled
IMPEDANCE	50Ω (nom)
AMPLITUDE ⁽⁵⁾	
Single-ended	1mV to 550mVpp
Differential	1mV to 1.1Vpp
AMPLITUDE RESOLUTION	1mV
AMPLITUDE ACCURACY	±(3% of amplitude ±2 mV)
RISE/FALL TIME (20% TO 80%)	< 60 ps (typ)
INSTANTANEOUS BANDWIDTH	
Standard	1.25GHz
x1 Mode	4.5GHz
MAX. USABLE FREQUENCY	2nd Nyquist, 8GHz
SKEW BETWEEN NORMAL AND COMPLEMENT OUTPUTS	0ps
JITTER (PEAK-PEAK)	<15 ps (typ)
CONNECTOR TYPE	SMA

SAMPLE CLOCK OUTPUT	
SOURCE	Selectable, internal synthesizer or sample clock input
FREQUENCY RANGE	SCLK Range
OUTPUT AMPLITUDE	0.5V to 1V depending on SCLK
IMPEDANCE	50Ω (nom), AC coupled
CONNECTOR	SMA

- (1) A sequence is defined as a continuous and looped series of tasks
(2) A scenario is defined as a continuous series of tasks/sequences
(3) SCLK=Max sample rate, amplitude = 400mVpp, Direct mode, measured using balun
(4) SCLK=Max sample rate, amplitude = 400mVpp, excluding SCLK/2-fout, measured using balun
(5) Double into open impedance

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SYNC CLOCK OUTPUT	
AMPLITUDE	500mVpp, typ.
FREQUENCY Standard x1 Mode	SCLK/8 SCLK/32
WAVEFORM	Square
RISE/FALL TIME (20% TO 80%)	<150ps
IMPEDANCE	LVC MOS
CONNECTOR	SMP

REFERENCE CLOCK OUTPUT	
SOURCE	Internal TCXO
WAVEFORM	Square
FREQUENCY	100MHz or REF IN
STABILITY	+/- 2.5 PPM
AGING	+/- 1 PPM @ +25°C (per year)
CONNECTOR	SMP

MARKER OUTPUTS		P9484/8/12B
NUMBER OF MARKERS		8/16/24
OUTPUT TYPE		Single Ended
OUTPUT IMPEDANCE		50Ω (nom)
AMPLITUDE		
VOLTAGE WINDOW		±1.15V
RESOLUTION		10mVpp
ACCURACY		±7%
OFFSET		
RANGE		±0.5V
RESOLUTION		10mV
ACCURACY		±(3% of setting+1% of amp ±15 mV)
RISE/FALL TIME (20% TO 80%)		<200ps
MARKER LENGHT		0 - waveform length
RESOLUTION Standard x1 Mode		2 pts 8 pts
MARKER DELAY		
COARSE DELAY		
RANGE		0 to 2048 points
RESOLUTION Standard x1 Mode		8 points 32 points
FINE DELAY		
RANGE		0 to 1.2ns
RESOLUTION		1ps
ACCURACY		15ps
CONNECTOR TYPE		SMP

DIGITIZER CHARACTERISTICS (AWT OPTION)	
NUMBER OF CHANNELS	1 or 2
INPUT VOLTAGE RANGE	500 mVpp (full scale)
INPUT VOLTAGE OFFSET	-2V to +2V
INPUT FREQUENCY RANGE	9GHz
RESOLUTION	12 bits
ACQUISITION MEMORY	Up to max. arbitrary memory
SAMPLE CLOCK SOURCES	Internal or external
INTERNAL CLOCK SOURCE	Internal, external reference
MAX SAMPLING RATE	5.4GS/s in Single channel mode 2.7Gs/s in Dual channel mode
MIN SAMPLING RATE	800MS/s
CLOCK ACCURACY	<2 ppm
IMPEDANCE	50Ω
COUPLING	DC or AC (factory configured)
CONNECTOR	SMA
TRIGGER SYSTEM	
TRIGGER MODES	Positive, negative edge
TRIGGER SOURCES	External, Software, Channel
COUPLING	DC
IMPEDANCE	50Ω (nominal)
LEVEL RANGE	>± 2.5 V (nominal)
FREQUENCY RANGE	DC to 65MHz
CONNECTOR	SMA

TRIGGER INPUTS	
RANGE	-5 V to +5 V
THRESHOLD	±5 V
RESOLUTION	100 mV
SENSITIVITY	200 mV
JITTER Standard x1 Mode Low Trigger Jitter Opt.	8 SCLK periods 32 SCLK periods SQRT(SCLK period ² + 150e-12 ²)
LATENCY / SYSTEM DELAY Standard x1 Mode	<900SCLK periods <2700 SCLK Periods
POLARITY	Pos or Neg
SOURCE	Selectable between channels
INPUT IMPEDANCE	10 kΩ or 50Ω (nom), DC coupled, factory configured
MAX TOGGLE FREQUENCY	10MHz (50MHz optional)
MINIMUM PULSE WIDTH	50ns (5ns optional)
CONNECTOR TYPE	SMP

REFERENCE CLOCK INPUT	
INPUT FREQUENCIES	10MHz / 100MHz selectable
LOCK RANGE	± 1MHz
INPUT LEVEL	0.6 Vp-p to 1.7 Vp-p
IMPEDANCE	50Ω, AC coupled (nom)
CONNECTOR TYPE	SMP

SAMPLE CLOCK INPUT	
FREQUENCY RANGE	SCLK Range
INPUT LEVEL RANGE	0.4Vpp to 1.2Vpp
DAMAGE LEVEL	<-0.5V or >1.5V
INPUT IMPEDANCE	50Ω nom, AC coupled
CONNECTOR TYPE	SMA

FAST SEGMENT DYNAMIC CONTROL INPUT (OPTIONAL)	
INPUT SIGNALS	Data 6 bit, Channel select 2 bit, Valid 1 bit
SEGMENTS / SEQUENCES	64 fast
DATA RATE	35MHz
MINIMUM LATENCY (Dynamic control input to direct out)	
FAST SEGMENT	<250ns
NORMAL SEGMENT	<1μ
INPUT LEVEL	LVTTL
CONNECTOR	D-SUB 9-Pin

DIGITAL UPCONVERTER	
MODES	NCO / Interpolation / IQModulation
SAMPLING RATE	1GS/s to Max sample rate
CARRIER FREQUENCY	
RANGE	0 to 40% of Sampling rate
RESOLUTION	48 bit
PHASE RANGE	0 to 360°
PHASE RESOLUTION	16 bit
INTERPOLATION FACTORS	x2, x4, x8
IQ FORMAT	<u>x2 Mode</u> <u>x4 Mode</u> <u>x8 Mode</u>
IQ PAIR PER CHANNEL	1 1 1 or 2
MAX INPUT RATE	2,500MS/s 2,250MS/s 1,125MS/s
NUMBER OF CHANNEL	2 2 4
SFDR AND HARMONICS	Same as Arbitrary
MEMORY	Same as Arbitrary

GENERAL	
VOLTAGE RANGE:	100 VAC to 264 VAC
FREQUENCY RANGE:	47Hz to 63Hz
POWER CONSUMPTION:	550W max.
FPGA TYPE	Xilinx Kintex UltraScale XCKU060 upgradeable to XCKU115
INTERNAL PROCESSING CPU MEMORY STORAGE OPERATING SYSTEM	Intel Pentium 3M Cache, 2.20GHz 8GB (Upgradeable) 120GB (Upgradeable) WINDOWS 10 IoT
INTERFACE: USB	1 x front panel USB host (type A) 2 x rear panel USB host, (type A) 1 x rear panel USB Device (type C)
Thunderbolt (Optional)	1 x rear panel Thunderbolt3
LAN (BASE-T)	1 x rear panel RJ45 1000/100/10
SFP+ (Optional, Replaces RJ45)	1 x rear panel SFP+ 10G Optical
GPIO (Optional)	IEEE 488.2 – GPIO
DISPLAY Built-in External	9" TFT touch LCD 1024x600 1 x rear panel HDMI
WEIGHT Without Package Shipping Weight	7.5 kg 9 kg
DIMENSIONS: With feet Without feet	440 X 175 x 330 mm (W x H x D) 440 X 190 x 330 mm (W x H x D)
TEMPERATURE: Operating Storage Warm up time	0°C to +40°C -40°C to +70°C 15 minutes
HUMIDITY:	85% RH, non-condensing
SAFETY:	CE Marked, EC61010-1:2010
EMC:	IEC 61326-1:2013
CALIBRATION:	2 years
WARRANTY*:	3 year standard * 1 year standard in India

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ORDERING INFORMATION

MODEL	DESCRIPTION
P9484B	9GS/s 16Bit 8GS Mem 4CH 8 Markers RF AWG Benchtop
P9488B	9GS/s 16Bit 8GS Mem 8CH 16 Markers RF AWG Benchtop
P94812B	9GS/s 16Bit 8GS Mem 12CH 24 Markers RF AWG Benchtop

OPTIONS

DC1	DC output for IQ baseband applications for model P9482B
DC2	DC output for IQ baseband applications for model P9484B
DC3	DC output for IQ baseband applications for model P9488
DC4	DC output for IQ baseband applications for model P94812B
DJ1	Dynamic Jump Input option for model P9482B
DJ2	Dynamic Jump Input option for model P9484B
DJ3	Dynamic Jump Input option for model P9488B
DJ4	Dynamic Jump Input option for model P94812B
MRK1	x8 Extra Markers option for model P9482B
MRK2	x8 Extra Markers option for model P9484B
MRK3	x16 Extra Markers option for model P9488B
LTJ1	Ultra Low Trigger Jitter (200ps typ.) option for model P9482B
LTJ2	Ultra Low Trigger Jitter (200ps typ.) option for model P9484B
LTJ3	Ultra Low Trigger Jitter (200ps typ.) option for model P9488B
LTJ4	Ultra Low Trigger Jitter (200ps typ.) option for model P94812B
G1	Low Waveform Granularity option for models P9482B
G2	Low Waveform Granularity option for model P9484B
G3	Low Waveform Granularity option for model P9488B
G4	Low Waveform Granularity option for model P94812B
AWT	5.4GS/s Single, 2.7GS/s Dual Channel 12 Bit Digitizer
SEC	Removable SSD
SSD	Extra Factory Duplicated Solid State Drive (SSD) for option SEC
TRG	Faster trigger input (50MHz instead of 10MHz)
STM	Streaming
TBolt	Rear panel Thunderbolt3 USB (type C)
SFP+	Rear panel 10G optical SFP+ connectivity (replace the LAN)

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