



CHANNELS CHARACTERISTICS	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D
NUMBER OF CHANNELS	2/4/6	2/4/8/12	2/4/8/12
INITIAL SKEW	<20ps		
FINE DELAY			
RANGE	0 to 5 ns		
RESOLUTION	5ps		
ACCURACY	±5ps		
COARSE DELAY			
RANGE	0 to wavelength		
RESOLUTION	1 sample point		

ARBITRARY MODE	P9082/4/6D	P2582/4/8/12D	P1282/4/8/12D
MAX. SAMPLE RATE	9GS/s	2.5GS/s	1.25GS/s
RESOLUTION	8-bit	16-bit	
MAX. MEMORY SIZE	Up to 16GS	Up to 8GS	
NUMBER OF SEGMENTS	64k		
MINIMUM SEGMENT LENGTH NORMAL FAST SEGMENT	2048 points 224 points	1024 points 64 points	
WAVEFORM GRANULARITY STANDARD OPTIONAL	64 points 32 points	32 points 16 points	32 points 16 points

TASK MODE	
TASK TABLE LENGTH	64K tasks per channel
TASK LOOPS	1M
SEQUENCE	A sequence is defined as a continuous and looped series of tasks
MAX. NUMBER OF SEQUENCES	32K sequences
SEQUENCE LOOPS	1M
SCENARIO	A scenario is defined as a continuous series of tasks/sequences
MAX. NUMBER OF SCENARIOS	1K scenarios

SIGNAL PURITY	DC OUTPUT	DIRECT OUTPUT
HARMONIC DISTORTION ⁽²⁾		
f _{out} = 10 MHz - 200 MHz, Measured @ DC to 2 GHz	<-70 dBc (typ.)	<-70 dBc (typ.)
f _{out} = 200 MHz ... 1.5 GHz, Measured @ DC to 4.5 GHz	<-60 dBc (typ.)	<-60 dBc (typ.)
f _{out} = 1.5 GHz ... 4.5 GHz, Measured @ DC to 4.5 GHz	<-50 dBc (typ.)	<-50 dBc (typ.)
SFDR ⁽³⁾		
f _{out} = 10 MHz...500 MHz, Measured @ DC to 1.5 GHz	-80 dBc (typ)	<-85 dBc (typ)
f _{out} = 500 MHz...4.5 GHz, Measured @ DC to 4.5 GHz	-70 dBc (typ)	<-75 dBc (typ)
PHASE NOISE (@10kHz offset)		
f _{out} = 140.625MHz	-134 dBc/Hz	
f _{out} = 280.25MHz	-128 dBc/Hz	
f _{out} = 562.5MHz	-122 dBc/Hz	
f _{out} = 1.125GHz	-116 dBc/Hz	
f _{out} = 2.25GHz	-110 dBc/Hz	
f _{out} = 4.5GHz	-104 dBc/Hz	

STREAMING (STM OPTION)	
MAX. STREAM RATE	3GS/s
MINIMUM PC REQUIREMENTS	
CPU	i7
MEMORY	32GB
OPERATING SYSTEM	WINDOWS 10 IoT
SOURCE	Internal / Rear panel interfaces

(1) Max input data rate is 2.5GS/s. Max. interpolating sample rate is 9GS/s

(2) SCLK=Max sample rate, amplitude = 400mVpp, Direct mode, measured using balun

(3) SCLK=Max sample rate, amplitude = 400mVpp, excluding SCLK/2-fout, measured using balun

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Infinite possibilities

DC OUTPUT	
OUTPUT TYPE	Single-ended or differential, DC-coupled
IMPEDANCE	50Ω (nom)
AMPLITUDE	50 mVp-p to 1.3 Vp-p
AMPLITUDE RESOLUTION	1mV
DC AMPLITUDE ACCURACY	±(3% of amplitude ±2 mV)
VOLTAGE WINDOW	±1.15V
DC OFFSET	±0.5V
OFFSET RESOLUTION	10mV
DC OFFSET ACCURACY	±(3% of setting ±15 mV)
SKEW BETWEEN NORMAL AND COMPLEMENT OUTPUTS	0ps
RISE/FALL TIME (20% TO 80%)	< 130 ps (typ)
INSTANTANEOUS BANDWIDTH P128xD P258xD P908xD	625MHz 1.25GHz 4.5GHz
MAX. USABLE FREQUENCY P128xD P258xD P908xD	2nd Nyquist 1.25GHz 2.5GHz 4.5GHz
JITTER (PEAK-PEAK)	<15 ps (typ)
OVERSHOOT	<5% (typ)
CONNECTOR TYPE	SMA

DIRECT OUTPUT (OPTIONAL)	
OUTPUT TYPE	Single-ended or differential, AC coupled
IMPEDANCE	50Ω (nom)
AMPLITUDE	1mVpp to 550mVpp
AMPLITUDE RESOLUTION	1mV
AMPLITUDE ACCURACY	±(3% of amplitude ±2 mV)
RISE/FALL TIME (20% TO 80%)	< 60 ps (typ)
INSTANTANEOUS BANDWIDTH P128xD P258xD P908xD	625MHz 1.25GHz 4.5GHz
MAX. USABLE FREQUENCY P128xD P258xD P908xD	2nd Nyquist 1.25GHz 2.5GHz 8GHz
CONNECTOR TYPE	SMA

SAMPLE CLOCK OUTPUT	
SOURCE	Selectable, internal synthesizer or sample clock input
FREQUENCY RANGE	SCLK Range
OUTPUT AMPLITUDE	0.5V to 1V depending on SCLK
IMPEDANCE	50Ω (nom), AC coupled
CONNECTOR	SMA

SYNC CLOCK OUTPUT	
AMPLITUDE	500mVpp, typ.
FREQUENCY P908xD P128xD, P258xD	SCLK/32 SCLK/8

SYNC CLOCK OUTPUT (Continued)	
WAVEFORM	Square
RISE/FALL TIME (20% TO 80%)	<150ps
IMPEDANCE	LVC MOS
CONNECTOR	SMP

MARKER OUTPUTS	
NUMBER OF MARKERS P1282D, P1284D P1288,P2582,P2584, P9082D P12812D P2588D, P9084D P25812D, P9086D	4 8 12 16 24
OUTPUT TYPE	Single Ended
OUTPUT IMPEDANCE	50Ω (nom)
AMPLITUDE	
VOLTAGE WINDOW	±1.15V
LEVEL	32mVpp to 1.2Vpp (32 discrete levels)
RESOLUTION	10mVpp
ACCURACY	±7%
OFFSET	
RANGE	±0.5V
RESOLUTION	10mV
ACCURACY	±(3% of setting ±15 mV)
RISE/FALL TIME (20% TO 80%)	<200ps
RANGE	0 - waveform length
RESOLUTION P128xD, P258xD P908xD	2 pts 8 pts
MARKER DELAY	
COARSE DELAY	
RANGE	0 to 2048 points
RESOLUTION P128xD, P258xD P908xD	8 points 32 points
FINE DELAY	
RANGE	0 to 1.2ns
RESOLUTION	1ps
ACCURACY	15ps
CONNECTOR TYPE	SMP

REFERENCE CLOCK OUTPUT	
SOURCE	Internal TCXO
WAVEFORM	Square
FREQUENCY	100MHz or REF IN
STABILITY	+/- 2.5 PPM
AGING	+/- 1 PPM @ +25°C (per year)
CONNECTOR	SMP

REFERENCE CLOCK INPUT	
INPUT FREQUENCIES	10MHz / 100MHz selectable
LOCK RANGE	± 1MHz
INPUT LEVEL	0.6 Vp-p to 1.7 Vp-p
IMPEDANCE	50Ω, AC coupled (nom)
CONNECTOR TYPE	SMP

SAMPLE CLOCK INPUT	
INPUT LEVEL RANGE	0.4Vpp to 1.2Vpp
DAMAGE LEVEL	< -0.5V or > 1.5V
INPUT IMPEDANCE	50Ω nom, AC coupled
CONNECTOR TYPE	SMA

TRIGGER INPUTS	
RANGE	-5 V to +5 V
THRESHOLD	±5 V
RESOLUTION	100 mV
SENSITIVITY	200 mV
JITTER	
Standard P128xD, P258xD P908xD	8 SCLK periods 32 SCLK periods
Low Trigger Jitter Opt.	$\sqrt{\text{SCLK period}^2 + 150\text{e-}12^2}$
LATENCY / SYSTEM DELAY	
P128xD, P258xD P908xD	<900SCLK periods <2700 SCLK Periods
POLARITY	Pos or Neg
SOURCE	Selectable between channels
INPUT IMPEDANCE	10 kΩ or 50Ω (nom), DC coupled, factory configured
MAX TOGGLE FREQUENCY	10MHz (50MHz Optional)
MINIMUM PULSE WIDTH	50ns (5ns Optional)
CONNECTOR TYPE	SMP

FAST SEGMENT DYNAMIC CONTROL INPUT (OPTIONAL)	
INPUT SIGNALS	Data 6bit, Channel select 2 bit, Valid 1 bit
SEGMENTS / SEQUENCES	1024 (128 fast)
DATA RATE	35MHz
MINIMUM LATENCY (Dynamic control input to direct out)	
FAST SEGMENT	<250ns
NORMAL SEGMENT	<1μ
INPUT LEVEL	LVTTTL
CONNECTOR	D-SUB, 9 pin

DIGITIZER CHARACTERISTICS (AWT OPTION)	
NUMBER OF CHANNELS	1 or 2
INPUT VOLTAGE RANGE	500 mVpp (full scale)
INPUT VOLTAGE OFFSET	-2V to +2V
INPUT FREQUENCY RANGE	9GHz
RESOLUTION	12 bits
ACQUISITION MEMORY	up to 8GS
SAMPLE CLOCK SOURCES	Internal or external
INTERNAL CLOCK SOURCE	Internal, external reference
MAX SAMPLING RATE	5.4GS/s in Single channel mode 2.7Gs/s in Dual channel mode
MIN SAMPLING RATE	1GS/s
CLOCK ACCURACY	<2 ppm
IMPEDANCE	50Ω
COUPLING	DC or AC (factory configured)
CONNECTOR	SMA
TRIGGER SYSTEM	
TRIGGER MODES	Positive, negative edge
TRIGGER SOURCES	External, Software, Channel
COUPLING	DC
IMPEDANCE	50Ω (nominal)
LEVEL RANGE	>± 2.5 V (nominal)
FREQUENCY RANGE	DC to 65MHz
CONNECTOR	SMA

FPGA PROGRAMMING	
FPGA TYPE	Xilinx Kintex UltraScale XCKU060 upgradeable to XCKU115
MODES	
STANDARD	Tabor standard built-in functionality
DECISION BLOCKS	Built-in library of mathematical functions, modulation & digital Filters
SHELL	Open core providing all interfaces and configuration path to the user

DIGITAL UPCONVERTER	
MODES	
P258X ALL OTHERS MODELS	NCO / Interpolation / IQModulation NCO only
SAMPLING RATE	1GS/s to Max sample rate
CARRIER FREQUENCY	
RANGE	0 to 40% of Sampling rate
RESOLUTION	48 bit
PHASE RANGE	0 to 360°
PHASE RESOLUTION	16 bit
INTERPOLATION FACTORS	x2, x4, x8
IQ FORMAT	x2 Mode
IQ PAIR PER CHANNEL	1
MAX INPUT RATE	1,250MS/s
NUMBER OF CHANNEL	2
SFDR AND HARMONICS	Same as Arbitrary
MEMORY	Same as Arbitrary

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GENERAL	
VOLTAGE RANGE:	
FREQUENCY RANGE:	47Hz to 63Hz
POWER CONSUMPTION:	550W max.
INTERFACE:	
USB	1 x front panel USB host (type A) 2 x rear panel USB host, (type A) 1 x rear panel USB Device (type C)
Thunderbolt (Optional)	1 x rear panel Thunderbolt3
LAN (BASE-T)	1 x rear panel RJ45 1000/100/10
SFP+ (Optional, Replaces RJ45)	1 x rear panel SFP+ 10G Optical
GPIB (Optional)	IEEE 488.2 – GPIB
STORAGE	120GB removable
WEIGHT	
Without Package	7.5 kg
Shipping Weight	9 kg
DIMENSIONS:	
With feet	175 X 221 x 316 mm (W x H x D)
Without feet	175 X 235 x 316 mm (W x H x D)
TEMPERATURE:	
OPERATING	0°C to +40°C
STORAGE	-40°C to +70°C
WARM UP TIME	15 minutes
HUMIDITY:	85% RH, non-condensing
SAFETY:	CE Marked, EC61010-1:2010
EMC:	IEC 61326-1:2013
CALIBRATION:	2 years
WARRANTY*:	3 year standard * 1 year standard in India

ORDERING INFORMATION	
MODEL	DESCRIPTION
P1282D	1.25GS/s, 16Bit, AWG, 1GS Memory, 2CH, 4 Markers
P1284D	1.25GS/s, 16Bit, AWG, 1GS Memory, 4CH, 4 Markers
P1288D	1.25GS/s, 16Bit, 2GS Memory, 8CH 8 Markers
P12812D	1.25GS/s, 16Bit, 2GS Memory, 12CH 12 Markers
P2582D	2.5GS/s, 16Bit, 2GS Memory 2CH, 8 Markers
P2584D	2.5GS/s, 16Bit, 2GS Memory, 4CH, 8 Markers
P2588D	2.5GS/s, 16Bit, 2GS Memory, 8CH 16 Markers
P25812D	2.5GS/s, 16Bit, 2GS Memory, 12CH, 24 Markers
P9082D	9GS/s 16Bit, 4GS Memory 2CH, 8 Markers
P9084D	9GS/s 16Bit, 4GS Memory 4CH, 16 Markers
P9086D	9GS/s 16Bit, 4GS Memory 6CH, 24 Markers

OPTIONS	
4M1	4GS Memory option for models P1282D & P2582D
4M2	4GS Memory option for models P1284D & P2584D
4M3	4GS Memory option for models P1288D, P2588D & P9084D
4M4	4GS Memory option for models P12812D, P25812D & P9086D
8M1	8GS Memory option for models P1282D & P2582D
8M2	8GS Memory option for models P1284D, P2584D & P9082D
8M3	8GS Memory option for models P1288D, P2588D & P9084D
8M4	8GS Memory option for models P12812D P25812D & P9086D

OPTIONS (Continued)	
16M1	16GS Memory option for models P9082D
16M2	16GS Memory option for models P9084D
16M3	16GS Memory option for models P9086D
DO1	9GHz BW Direct Output option for models P1282D & P2582D
DO2	9GHz BW Direct Output option for models Pxx84D & P9082D
DO3	9GHz BW Direct Output option for models Pxx88D & P9084D
DO4	9GHz BW Direct Output option for models Pxx812D & P9086D
DJ1	Dynamic Jump Input option for models P1282D & P2582D
DJ2	Dynamic Jump Input option for P1284D, P2584D & P9082D
DJ3	Dynamic Jump Input option for P1288D, P2588D & P9084D
IM1	Interpolation Mode option for P258D (x2 and x4)
IM2	Interpolation Mode option for P908D (x2, x4 and x8)
MRK1	x8 Extra Markers option for models P1282D & P2582D
MRK2	x8 Extra Markers option for models P1284D P2584D & P9082D
MRK3	x16 Extra Markers option for models P1288D P2588D & P9084D
LTJ1	Ultra Low Trigger Jitter (200ps typ.) option for models P1282D & P2582D
LTJ2	Ultra Low Trigger Jitter (200ps typ.) option for models P1284D P2584D & P9082D
LTJ3	Ultra Low Trigger Jitter (200ps typ.) option for models P1288D, P2588D & P9084D
LTJ4	Ultra Low Trigger Jitter (200ps typ.) option for models P12812D, P25812D & P9086DD
G1	Low Waveform Granularity option for models P1282D & P2582D
G2	Low Waveform Granularity option for P1284B, P2584D & P9082D
G3	Low Waveform Granularity option for P1288D, P2588D & P9084D
G4	Low Waveform Granularity option for P12812D, P25812D & P9086D
AWT	5.4GS/s Single, 2.7GS/s Dual Channel 12 Bit Digitizer option for models P1284D, P1288D, P2584D, P2588D, P9082D & P9084D
STM	3GS/s Streaming option
DUC	Digital UpConverter for models P2582D, P2584D, P2588D & P25812D
SEC	Removable SSD option for security needs for all models
SSD	Extra Factory Duplicated Solid State Drive (SSD) for option SEC
TRG	Faster trigger input (50MHz instead of 10MHz)
PROG	High level FPGA programming capability through decision blocks of built-in Demodulation & digital Filters
Shell	Programmable FPGA with open core for user embedded IP
TBolt	Rear panel Thunderbolt3 USB (type C)
SFP+	Rear panel 10G optical SFP+ connectivity (replace the LAN)

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