

PRELIMINARY

40 Years
Celebration

New



MODEL 2816

300Mb/s, 16-Channels
Digital Signal Amplifier/POD

- 16 Digital Data Channels
- Data Rate of 1.5b/s to 300Mb/s
- Dedicated pattern memory of 128k/channel or up to 4M shared Arbitrary Memory
- Programmable amplitude and offset control
- <2ns Initial skew between bits with ± 2.5 ns skew control and 5ps resolution
- Up to 5Vp-p into 50 Ω , double into open circuit
- Transition Times < 2ns
- Auxiliary clock output

The 2816 is a data generator pod that operates only in conjunction with one of the following Tabor generators: WW2571A and WW2572A, PM8571A and PM8572A. Combined they become a fully featured, high performance, 300Mb/s 16 Channels Digital Data Generator that provides a flexible and innovative solution answering the needs of today's digital designers in performing characterization, validation, verification or debugging of circuits.

Pattern Memory

The Data Generator combo offers two pattern memory options: the data can be stored on a dedicated 128k per channel pattern memory or alternatively for longer patterns the arbitrary memory of up to 4M can be used, allowing the user to work in parallel with arbitrary waveforms.

Independent Level Control

As standard, the generator comes with a rear-panel connector that feeds 16 LVDS data channels into matching 100 loads. The 2816 pod converts the LVDS levels from the waveform generator output to any level within a 5Vp-p amplitude window. Each channel has its own controls, so it is possible to create unique levels for a single or groups of data channels, extending the use of the standard generators to a vast array of digital design applications.

Digital and Analog Channels Simultaneously

While the digital data can be generated through the 2816, analog waveforms can be generated simultaneously from the front panel of the Tabor Generator. Users can either generate analog waveforms and their digital representation, or using the 2-channel Tabor generator to create 16-bits of digital data from channel 1 and independent analog waveforms from channel 2.

Pattern Editing and Sequencing

The 2816 provides an ideal solution for applications that require characterization of a circuit or device timing as well as amplitude margins. Using the Pattern Composer, which is included with the ArbConnection software, any complex data pattern can be designed quickly and easily. Using the sequencer enables insertion of infrequent faults and glitches into the data patterns to verify device or circuit recovery.

Target Applications

Target applications include parallel communication test between devices, characterize device timing, characterize and verify Asic, FPGA and DACs, D/A communication for cell phones and digital TV, create complex data patterns using the sequencer, test LCD display drivers, and more.

Visit our website at www.taborelec.com

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TABOR ELECTRONICS Inc.
Since 1971

MODEL 2816



300Mb/s, 16-Channels
Digital Signal Amplifier/POD

Specification

DATA OUTPUTS CHARACTERISTICS

| | |
|----------------------------------|--------------------------|
| Channels: | 16 |
| Type: | Single-ended |
| Connector: | SMB |
| Impedance: | 50Ω, ±1% |
| Skew Control Between bits | |
| Control | Independent for each bit |
| Initial Skew | 2ns max |
| Range | -2.5ns to +2.5ns |
| Resolution | 5ps |
| Accuracy | ±(10% of setting +20ps) |
| Data Rate: | |
| DC to 100Mb/s | 5Vp-p |
| DC to 200Mb/s | 3.3Vp-p |
| DC to 300Mb/s | 2Vp-p |
| VOH: | -0.5V to +3.5V into 50Ω |
| VOL: | -1.5V to +2.5V into 50Ω |
| Resolution: | 0.02V |
| Maximum Swing: | 5Vp-p into 50Ω |
| Minimum Swing: | 1Vp-p into 50Ω |
| Output Current: | |
| Sink | > -20mA per channel |
| Source | <+40mA per channel |
| Amplitude: | 5Vp-p into 50Ω |
| Rise/Fall time: | <2ns |

SAMPLE CLOCK OUTPUT

| | |
|------------------------|---|
| Connector: | Front panel SMB |
| Sampling Rate: | 2.5MHz to 250MHz (300MHz, typ.) |
| Resolution: | 9 digits |
| Level: | 500mVp-p into 50Ω double into high impedance |
| Rise/Fall Time: | <1 ns into 50Ω |
| Impedance: | 50Ω, ±1% |

DIGITAL DATA INPUT

| | |
|------------------------|------------------------------------|
| Connector: | Rear panel SCSI-2 type 68-pin VHDC |
| Pattern Width: | 16-bits, differential |
| Level: | LVDS |
| Pattern Source: | Programmed by 257XA/857X |
| Pattern Length: | |
| Dedicated | 128k per channel |
| Arbitrary | Up to 4M |

CONTROL LINE INPUTS

| | |
|-------------------|--------------------------|
| DATA | |
| Connector: | Rear panel SMB |
| Impedance: | 1MΩ |
| Input: | COUPLE IN of 257XA/857XA |

STRB

| | |
|-------------------|---------------------------|
| Connector: | Rear panel SMB |
| Impedance: | 1MΩ |
| Input: | COUPLE OUT of 257XA/857XA |

CLK

| | |
|-------------------|------------------------|
| Connector: | Rear panel SMB |
| Impedance: | 1MΩ |
| Input: | SCLK IN of 257XA/857XA |

SCLK

| | |
|-------------------|-------------------------|
| Connector: | Rear panel SMB |
| Impedance: | 10kΩ |
| Input: | SCLK OUT of 257XA/857XA |

GENERAL

| | |
|---------------------------------|----------------------------|
| Voltage Ranges: | 85 to 265 Vac |
| Frequency Range: | 47Hz to 63Hz |
| Maximum Current: | 3 A |
| Dimensions: | |
| With Feet | 315 x 102 x 395 mm (WxHxD) |
| Without Feet | 315 x 88 x 395 mm (WxHxD) |
| Weight: | |
| Without Package | 3.5kg |
| Shipping Weight | 4kg |
| Temperature: | |
| Operating | 0°C to 50°C |
| Storage | -40°C to 70°C |
| Humidity: | 80% RH, non condensing |
| Safety: | CE Marked, IEC61010-1 |
| Calibration: | 1 years |
| Warranty ⁽¹⁾: | 3 years standard |

ORDERING INFORMATION

| MODEL | DESCRIPTION |
|-------------|--|
| 2816 | 300Mb/s, 16-Channels Digital Signal Amplifier/POD |

⁽¹⁾ Standard warranty in India is 1 year.