

# **MODELS WS8351/2**

# 350MHz Single/Dual Channel Arbitrary Function Generators

### **Specification**

CONFIGURATION

Output Channels 1/2, Synchronized/fully

separated

STANDARD WAVEFORMS

**Type:** Sine, triangle, square,

ramp, pulse, sin(x)/x, exponential rise, exponential decay, gaussian, noise and DC.

Frequency Range:

Sine, Pulse 1µHz to 350MHz Square 1µHz to 250MHz All others 1µHz to 125MHz

SINE

Start Phase: 0 to 360°

Harmonics Distortion (typ.):

5MHz to 200MHz <-40dBc 200MHz to 350MHz<sup>(1)</sup> <-50dBc<sup>(1)</sup>

(1) Measured with 500MHz lowpass fiter

Non-Harmonics Distortion (typ.):

1MHz to 100MHz <-80dBc

100MHz to 250MHz <-75dBc 250MHz to 350MHz <-70dBc

SSB Phase Noise (10kHz offset):

1MHz Carrier <-120dBc/Hz 10MHz Carrier <-118dBc/Hz 100MHz Carrier <-115dBc/Hz 250MHz Carrier <-108dBc/Hz 350MHz Carrier <-100dBc/Hz

TRIANGLE / RAMP (SAW-TOOTH)

Start Phase: 0 to 360° Phase Resolution: 0.1°

**Timing Range:** 1.0% to 99.9%

**SQUARE** 

Duty cycle Range: 1.0% to 99.9%

Rise/Fall time: 1ns (typically <900ps)

Overshoot: <5%, typ. Jitter (rms): <10ps

SINC (Sine(x)/x)

"O Crossings" 4 to 100 cycles

GAUSSIAN

Time Constant 10 to 200

**EXPONENTIAL PULSE** 

**Type:** Rise or Decay, selectable

Time Constant: -100 to 100

NOISE

Bandwidth: 125MHz

DC

Range: -2V to +2V

**PULSE** 

Polarity:

Pulse Mode: Single or double,

programmable Normal, inverted or

Period: complement
4ns to 1.6s
Resolution: 1ns
Pulse Width: 2ns to 1.6s

Rise/Fall Time:

Fast 1ns (typical < 900ps)

Linear 1ns to 1.6s

Delay & Double

Pulse Delay: 1ns to 1.6s

Amplitude Window: 100mVp-p to 4Vp-p into

50Ω

Levels

Low Level -2V to +1.95V High Level -1.95V to +2V

NOTES:

1. All pulse parameters, except rise and fall times, may be freely programmed within the selected pulse period provided that the ratio between the period and the smallest incremental unit does not exceed the ratio of 512.000 to 1.

2. Rise and fall times, may be freely programmed provided that the ratio between the rise/fall time and the smallest incremental unit does not exceed the ratio of 1,000,000 to 1.

**3.** The sum of all pulse parameters must not exceed the pulse period setting.

#### PULSE / PATTERN COMPOSER

#### MULTI-LEVEL / LINEAR-POINTS

Number of Levels: 1 to 1000

Dwell Time: 500ps to 10s

Transition type: Fast or Linear

Memory: 100k

Amp. Resolution: 4 digits

Time Resolution: 500ps to 100ns (auto or

user)

#### PATTERN

Preamble:

Pattern Source: PRBS or user-defined PRBS Type: PRBS7, PRBS9, PRBS11, PRBS15, PRBS23, PRBS31, USER

1 to 512e3

Data Rate: 10Bit/s to 350MBit/s

Number of Levels: 2, 3, 4, 5 High/Low Levels: ±2.5V Resolution: 4 digits Loops: 1 to 1e6 **Length:** 1 to 512e3

#### PAM (PULSE AMPLITUDE MODULATION)

**Data Rate:** 10Mbit/s to 2Gbit/s

PAM Range: 2 to 1000 Pattern Memory: 16Mbit Resolution: 1 bit (TBD)

#### ARBITRARY WAVEFORMS

**Sample Rate:** 10MS/s to 2GS/s

Vertical Resolution: 14 bits

Waveform Memory: 512kpointsstandard

Min. Segment Size: 192 points Resolution: 16 points No. of Segments: 1 to 16k Waveform Granularity: 1 point

#### **MODULATION**

#### COMMON CHARACTERISTICS

Carrier Waveform: Sine, square, triangle Carrier Frequency: 10kHz to 350MHz

Modulation Source: Internal

FΜ

Modulation Shape: Sine, square, triangle,

ramp

Modulation Freq.: 100Hz to 35MHz Deviation Range: 10mHz to 175MHz

#### FSK / FREQUENCY HOPPING

**FSK Baud Rate:** 10mbps to 350Mbps

**Hop Table Size:** 2 to 256 **Hop Type:** Fast or Linear

**Dwell Time Mode:** Fixed or programmable

per step

Dwell Time: 2ns to 10s Dwell Time Res.: 2ns

SWEEP / CHIRP

Sweep Type: Linear or log Sweep Direction: Up or down Sweep Time: 1.4 µs to 10ms

Modulation Shape: Pulse

Pulse Repetition:

Range 200ns to 20s Resolution 3 digits Accuracy 100ppm

AΜ

Modulation Shape: Sine, square, triangle,

amp

Modulation Freq.: 100Hz to 1MHz Modulation Depth: 0.1 to 200%

ASK / AMPLITUDE HOPPING

Hop Table Size: 10mbps to 350Mbps

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### 350MHz Single/Dual Channel **Arbitrary Function Generators**

### **Specification**

Hop Table Size: Hop Type: Fast or Linear

Dwell Time Mode: Fixed or programmable

per step

**Dwell Time:** 2ns to 10s Resolution 2ns

**COMMON CHARACTERISTICS** 

**FREQUENCY** 

Resolution: 8 digits

Accuracy/Stability: Same as reference

ACCURACY REFERENCE CLOCK:

1 ppm from 19°C to 29°C: 1ppm/°C below 19°C or above 29°C; 1 ppm/

year aging rate

External -5dBm to 5dBm,  $50\Omega$ 

**AMPLITUDE** 

Range:

Single-ended 50mVp-p to 4Vp-p\* Differential 100mVp-p to 8Vp-p\*

\* Double into high impedance Resolution: 4 diaits Accuracy: ±(3% +5mV) 1ns (<900ps typ.) Rise/Fall Time:

Overshoot: 5%, typ.

**OFFSET** 

-1.5V to + 1.5V into  $50\Omega$ Offset Range:

Offset Resolution: 4 digits Offset Accuracy:  $\pm(5\% + 5mV)$ 

**OUTPUTS** 

MAIN OUTPUTS

Coupling: DC-coupled Type: Single ended or

differential

Connectors: Front panel SMAs

Impedance:  $500 \pm 1\%$ 

Protected against Protection: temporary short to case ground

SYNC OUTPUT

Connector: Front panel SMA Source: Channel 1 or channel 2

Single ended Type:

Waveform Type:

Pulse 16 points width WCOM Waveform complete

Impedance:

Amplitude: 1V; doubles into high

impedance

Variable Position Control:

0 to segment length Range

Resolution 16 points Rise/Fall Time 2ns, typical Variable Width control:

Range 16 points to segment

length

Resolution 16 points

**INPUTS** 

TRIGGER INPUT

Rear panel SMA Connector:

Input Impedance:  $10k\Omega$ 

Polarity: Positive, negative, or both

Damage Level: ±20Vdc Frequency Range: 0 to 15MHz

**Trigger Level Control:** 

Range -5V to 5V Resolution 12 bit (2.5mV)

 $\pm$ (5% of setting + 2.5mV) Accuracy

Sensitivity 0.2Vp-p Min. Pulse Width: 10 ns

**EVENT INPUT** 

Connector: Rear panel BNC

Input Impedance: 10kΩ

Polarity: Positive, negative or

either

Damage Level: ±20Vdc Frequency Range: 0 to 15MHz **Trigger Level Control:** 

-5V to 5V Range

Resolution 12 bit (2.5mV) Accuracy  $\pm$ (5% of setting + 2.5mV)

Sensitivity 0.2Vp-p minimum

Min. Pulse Width: 10ns

**EXTERNAL REFERENCE INPUT** 

Connector: Rear panel BNC Input Frequency: 10MHz to 100MHz

Input Impedance: 50Ω

Voltage Swing: -5dBm to 5dBm

Damage Level: 10dBm

EXTERNAL SAMPLE CLOCK INPUT

Rear panel SMA Connector:

Input Impedance: 500

0dBm to 10dBm Voltage Swing: Input Frequency: 1GHz to 4GHz (Double the

internal clock)

Clock Divider: 1/1, 1/2, 1/4, 1/256,

channel separate for each Damage Level: 15dBm

**RUN MODES** 

Continuous: A selected output function shape is output continuously. Self Armed: No start commands are

required to generate waveforms.

Armed: The output dwells on a DC level and waits for an enable command and then the output waveform is output continuously; An abort command turns off the waveform.

Triggered: A trigger signal activates a single-shot or counted burst of output waveforms and then the instrument waits

for the next trigger signal.

Normal Mode: The first trigger signal activates the output; consecutive triggers are ignored for the duration of the output waveform

Override Mode: The first trigger signal activates the output; consecutive triggers restart the output waveform regardless if the current waveform has been completed

Gated: A waveform is output when a gate signal is asserted. The waveform is repeated until the gate signal is

de-asserted.

or not.

Last period is always

completed. Burst: Upon trigger, outputs a or multiple pre-Dual programmed number of waveform

from 1 through 1M. cycles

TRIGGER CHARACTERISTICS

**EXTERNAL** 

Source: Channel 1, channel 2, or

both

Connector: SMA Input Impedance: 10kΩ

Polarity: Positive, negative, or both

Damage Level: ±20VDC Frequency Range: 0 to 15MHz

Trigger Level Control: Range -5V to 5V

Resolution 12 bit (2.5mV)

Accuracy  $\pm$ (5% of setting + 2.5mV)

Sensitivity 0.2Vp-p

Pulse Width: 10 ns, minimum

System Delay: 200 SCLK periods + 50ns Trigger Delay: Separate for each channel 0 to 8,000,000 SCLK Range

periods

Resolution 4 points

Accuracy Same as SCLK accuracy Smart Trigger: Detects a unique pulse

width

Conditioned Trigger: <pul><pulsewidth,>pulse

width or <>pulse width

Pulse Width Range 50ns to 2s



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### **Specification**

Resolution

 $\pm$ (5% of setting +20ns) Accuracy Trigger Hold-off: Ignores triggers for a

hold-off

Hold-off range 100ns to 2s

Resolution 2ns

 $\pm$ (5% of setting +20ns) Accuracy Trigger jitter: 2ns at max. SCLK (4 SCLK)

INTERNAL

Common or separate Source: Modes:

Waveform start to Timer

waveform start Delayed

waveform start Timer:

Range

Resolution Accuracy

periods Resolution

by 4

Delay Range

Even numbers, divisible

152 to 8,000,000 SCLK

Waveform stop to

200ns to 2s

3 digits

100ppm

MANUAL

Source: Soft trigger command from the front panel or remote

0 to waveform-length

INTER-CHANNEL SKEW CONTROL

**COURSE TUNING** 

Initial skew: 200ps Control:

Range points

Resolution 4 points

Accuracy: Same as SCLK accuracy

**FINE TUNING** 

Initial skew: 200ps Control:

Range -3ns to +3ns Resolution 10ps

Accuracy: (10% of setting + 20ps)

**GENERAL** 

100VAC to 240VAC Voltage Range: Frequency Range: 50Hz to 60Hz Power Consumption: 150VA

Display Type: TFT LCD, back-lit Size Resolution 320 x 240 pixels

Interfaces:

USB 2.0 Host 1 x front, USB host, (A

type);

1 x rear, USB device, (B Device

type) IAN

1000/100/10 BASE-T **GPIB** IEEE 488.2 standard

interface Dimensions:

With Feet 315 x 102 x 395 mm

(WxHxD)

Without Feet 315 x 88 x 395 mm (WxHxD)

Weight:

Without Package4.5kg Shipping Weight 6kg

Temperature:

Operating 0°C to 40°C Storage -40°C to 70°C

**Humidity:** 85% RH, non condensing CE Marked. IEC61010-1 Safety: EMC: IEC 61326-1:2006

Calibration: 2 years

Warranty (1): 3 years standard

ORDERING INFORMATION

MODEL **DESCRIPTION** WS8351 350MHz Single Channel Arbitrary Function Generator WS8352 350MHz Dual Channel

Arbitrary Function Generator

**ACCESSORIES** 

Multi-instrument Sync Cable:

synchronization

S-Rack Mount: 19" Single Rack Mounting

Case Kit: Professional Carrying Bag

Options and Accessories Note: must be specified at the time of your

purchase

<sup>(1)</sup> Standard warranty in India is 1 year.